

October 2014

# **BC327 PNP Epitaxial Silicon Transistor**

#### **Features**

- Switching and Amplifier Applications
- Suitable for AF-Driver Stages and Low-Power Output Stages
- · Complement to BC337 / BC338



# **Ordering Information**

Part Number	Top Mark	Package	Packing Method	
BC327BU	BC327	TO-92 3L	Bulk	
BC32716BU	BC32716	TO-92 3L	Bulk	
BC32716TA	BC32716	TO-92 3L	Ammo	
BC32725BU	BC32725	TO-92 3L	Bulk	
BC32725TA	BC32725	TO-92 3L	Ammo	
BC32740BU	BC32740	TO-92 3L	Bulk	
BC32740TA	BC32740	TO-92 3L	Ammo	

# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}\text{C}$  unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CES</sub>	Collector-Emitter Voltage	-50	V
V <sub>CEO</sub>	Collector-Emitter Voltage	-45	V
V <sub>EBO</sub>	Emitter-Base Voltage	-5	V
I <sub>C</sub>	Collector Current (DC)	-800	mA
TJ	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	-55 to 150	°C

# Thermal Characteristics(1)

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
D	Power Dissipation	625	mW
P <sub>D</sub>	Derate Above 25°C	5.0	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	200	°C/W

### Note:

1. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

# **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	$I_C = -10 \text{ mA}, I_B = 0$	-45			V
BV <sub>CES</sub>	Collector-Emitter Breakdown Voltage	$I_C = -0.1 \text{ mA}, V_{BE} = 0$	-50			V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = -10 \mu A, I_C = 0$	-5			V
I <sub>CES</sub>	Collector Cut-Off Current	$V_{CE} = -45 \text{ V}, I_{B} = 0$		-2	-100	nA
h <sub>FE1</sub>	DC Current Gain	$V_{CE} = -1 \text{ V}, I_{C} = -100 \text{ mA}$	100		630	
h <sub>FE2</sub>	DO Current Gain	$V_{CE} = -1 \text{ V, } I_{C} = -300 \text{ mA}$	60			
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	$I_C = -500 \text{ mA}, I_B = -50 \text{ mA}$			-0.7	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage	$V_{CE} = -1 \text{ V}, I_{C} = -300 \text{ mA}$			-1.2	V
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE}$ = -5 V, $I_{C}$ = -10 mA, $f$ = 20 MHz		100		MHz
C <sub>ob</sub>	Output Capacitance	$V_{CB} = -10 \text{ V}, I_{E} = 0,$ f = 1 MHz		12		pF

# h<sub>FE</sub> Classification

Classification	16	25	40
h <sub>FE1</sub>	100 ~ 250	160 ~ 400	250 ~ 630
h <sub>FE2</sub>	60 ~	100 ~	170 ~

# **Typical Performance Characteristics**

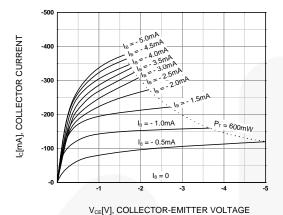


Figure 1. Static Characteristic

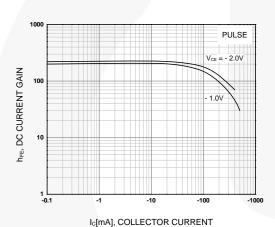


Figure 3. DC current Gain

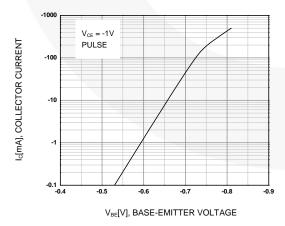


Figure 5. Base-Emitter On Voltage

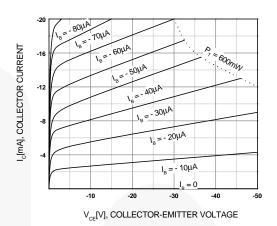


Figure 2. Static Characteristic

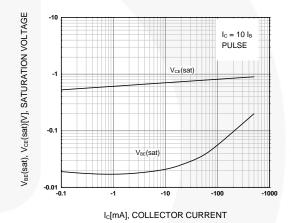


Figure 4. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

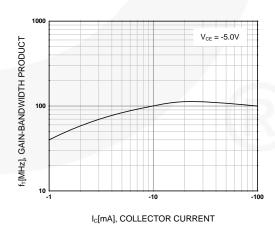
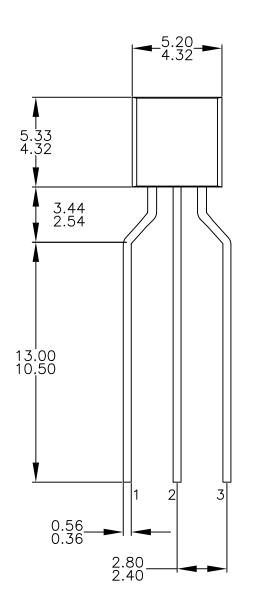
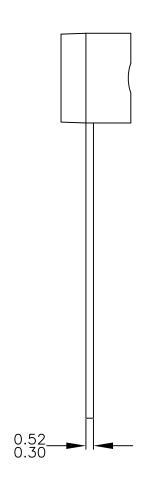
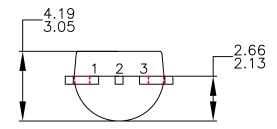


Figure 6. Gain Bandwidth Product

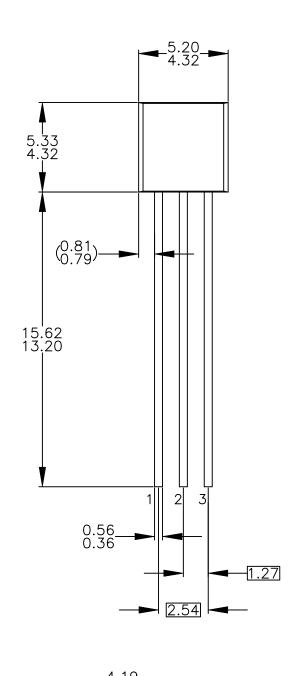


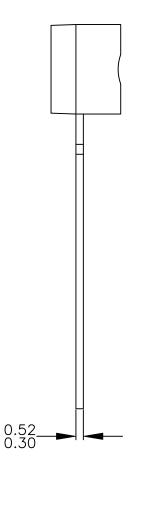




## NOTES: UNLESS OTHERWISE SPECIFIED

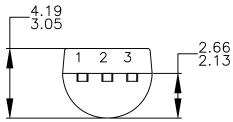
- DRAWING CONFORMS TO JEDEC MS-013, VARIATION AC.
  ALL DIMENSIONS ARE IN MILLIMETERS.
  DRAWING CONFORMS TO ASME Y14.5M-2009.
  DRAWING FILENAME: MKT-ZAO3FREV3.
  FAIRCHILD SEMICONDUCTOR.
- B. C. D. E.





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- DRAWING WITH REFERENCE TO JEDEC TO-92 RECOMMENDATIONS.
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  DRAWING CONFORMS TO ASME Y14.5M-2009.
  DRAWING FILENAME: MKT-ZAO3DREV4.









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