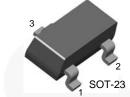


August 2015

# BC846 / BC847 / BC848 / BC850 NPN Epitaxial Silicon Transistor

### **Features**

- Switching and Amplifier Applications
- · Suitable for Automatic Insertion in Thick and Thin-film Circuits
- Low Noise: BC850
- Complement to BC856, BC857, BC858, BC859, and BC860



1. Base 2. Emitter 3. Collector

## Ordering Information(1)

Part Number	Marking	Package	Packing Method	
BC846AMTF	8AA	SOT-23 3L	Tape and Reel	
BC846BMTF	8AB	SOT-23 3L	Tape and Reel	
BC846CMTF	8AC	SOT-23 3L	Tape and Reel	
BC847AMTF	8BA	SOT-23 3L	Tape and Reel	
BC847BMTF	8BB	SOT-23 3L	Tape and Reel	
BC847CMTF	8BC	SOT-23 3L	Tape and Reel	
BC848BMTF	8CB	SOT-23 3L	Tape and Reel	
BC848CMTF	8CC	SOT-23 3L	Tape and Reel	
BC850AMTF	8EA	SOT-23 3L	Tape and Reel	
BC850CMTF	8EC	SOT-23 3L	Tape and Reel	

## Note:

1. Affix "-A,-B,-C" means h<sub>FE</sub> classification. Affix "-M" means SOT-23 package. Affix "-TF" means the tape and reel type packing.

1

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parame	Value	Unit		
		BC846	80		
$V_{CBO}$	Collector-Base Voltage	BC847 / BC850	50	V	
		BC848	30		
		BC846	65		
$V_{CEO}$	Collector-Emitter Voltage	BC847 / BC850	45	V	
		BC848	30		
V	Emitter Bose Voltage	BC846 / BC847	6	V	
V <sub>EBO</sub> Emitter-Base	Emitter-Base Voltage	BC848 / BC850	5	v	
I <sub>C</sub>	Collector Current (DC)		100	mA	
TJ	Junction Temperature		150	°C	
T <sub>STG</sub>	Storage Temperature Range		-65 to +150	°C	

## Thermal Characteristics(2)

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Unit
D	Power Dissipation	310	mW
P <sub>D</sub>	Derate Above 25°C	2.48	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	403	°C/W

### Note:

2. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

## Electrical Characteristics(3)

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter		Conditions	Min.	Тур.	Max.	Unit
I <sub>CBO</sub>	Collector Cut-Off Current		$V_{CB} = 30 \text{ V}, I_{E} = 0$			15	nA
h <sub>FE</sub>	DC Current Gain		$V_{CE} = 5 \text{ V}, I_{C} = 2 \text{ mA}$	110		800	
\/(cat)	Collector-Emitter Saturation Voltage		$I_C = 10 \text{ mA}, I_B = 0.5 \text{ mA}$		90	250	mV
vCE(sat)			$I_C = 100 \text{ mA}, I_B = 5 \text{ mA}$		200	600	
\/(eat)	V <sub>BE</sub> (sat) Collector-Base Saturation Voltage		$I_C = 10 \text{ mA}, I_B = 0.5 \text{ mA}$		700		mV
v <sub>BE</sub> (sat)			$I_C = 100 \text{ mA}, I_B = 5 \text{ mA}$		900		IIIV
\/(on)	V <sub>BE</sub> (on) Base-Emitter On Voltage		$V_{CE} = 5 \text{ V}, I_{C} = 2 \text{ mA}$	580	660	700	mV
vBE(OII)			$V_{CE} = 5 \text{ V}, I_{C} = 10 \text{ mA}$			720	1110
f <sub>T</sub>	Current Gain Bandwidth Product		$V_{CE} = 5 \text{ V, } I_{C} = 10 \text{ mA,}$ f = 100 MHz		300		MHz
C <sub>ob</sub>	Output Capacitance		$V_{CB} = 10 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$		3.5	6.0	pF
C <sub>ib</sub>	Input Capacitance		$V_{EB} = 0.5 \text{ V}, I_{C} = 0, f = 1 \text{ MHz}$		9		pF
		BC846 / BC847 / BC848	$V_{CE} = 5 \text{ V}, I_{C} = 200 \mu\text{A},$		2.0	10.0	
NF	Noise	BC850	$R_G = 2 k\Omega, f = 1 kHz$		1.2	4.0	dB
Figu	Figure	BC850	$V_{CE} = 5 \text{ V}, I_{C} = 200 \mu\text{A}, R_{G} = 2 \text{ k}\Omega, f = 30 \text{ to } 15000 \text{ Hz}$		1.4	3.0	QD.

### Note:

3. Pulse test: pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2\%$ 

# h<sub>FE</sub> Classification

Classification A		В	С
h <sub>FE</sub>	110 ~ 220	200 ~ 450	420 ~ 800

## **Typical Performance Characteristics**

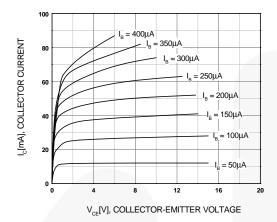


Figure 1. Static Characteristic

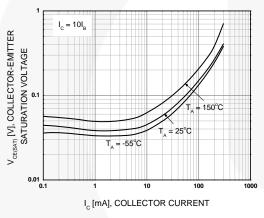


Figure 3. Currector-Emitter Saturation Voltage

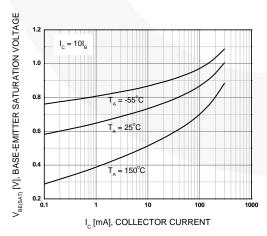


Figure 5. Base-Emitter Saturation Voltage

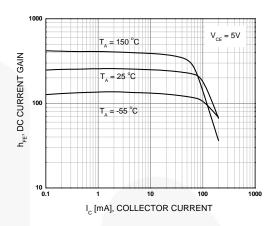


Figure 2. DC Current Gain

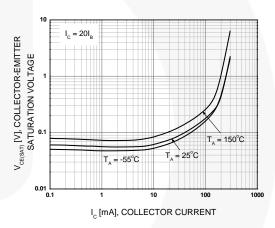


Figure 4. Currector-Emitter Saturation Voltage

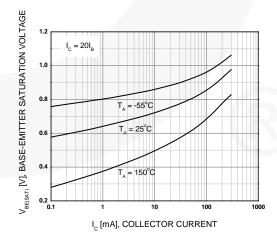


Figure 6. Base-Emitter Saturation Voltage

## **Typical Performance Characteristics** (Continued)

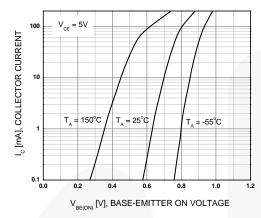
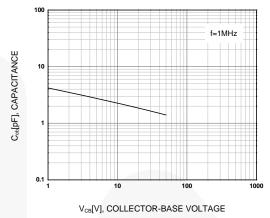


Figure 7. Base-Emitter On Voltage



**Figure 8. Collector Output Capacitance** 

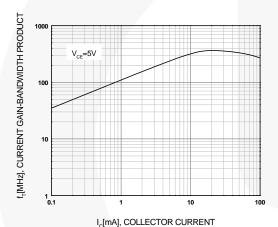
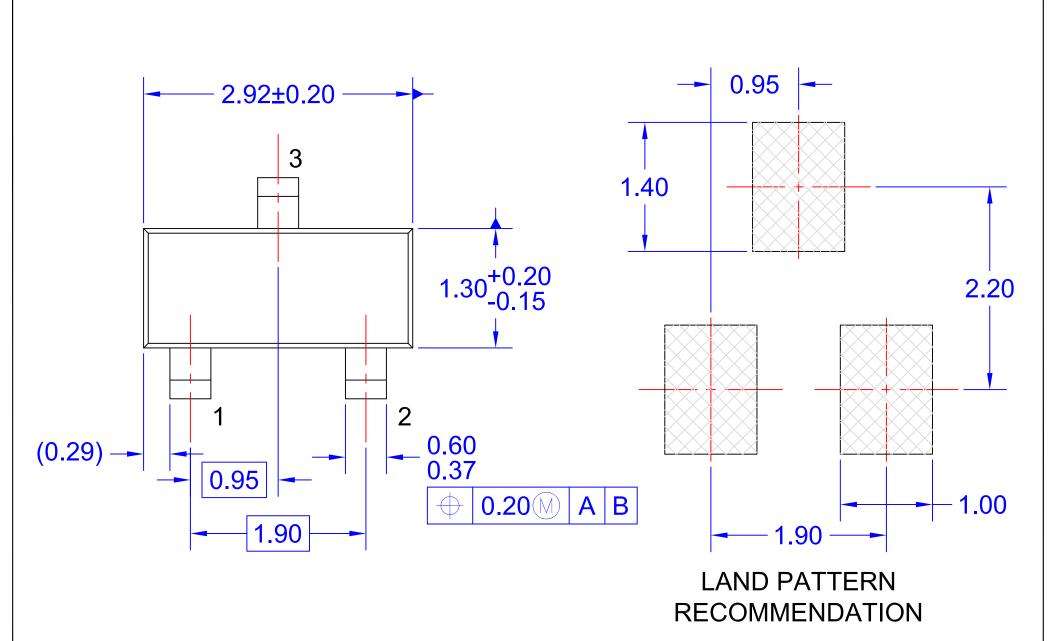
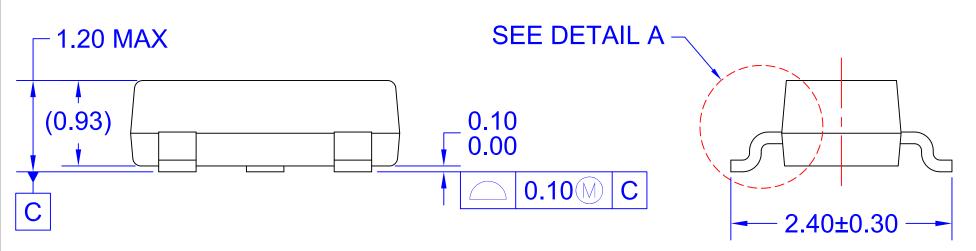
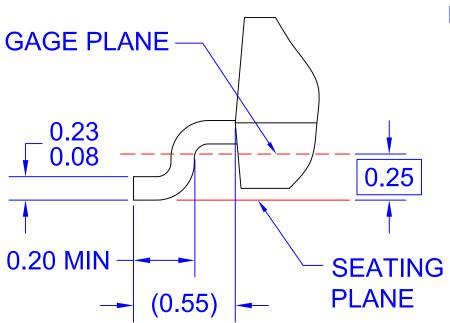


Figure 9. Current Gain Bandwidth Product







DETAIL A
SCALE: 2X

# NOTES: UNLESS OTHERWISE SPECIFIED

- A) REFERENCE JEDEC REGISTRATION TO-236, VARIATION AB, ISSUE H.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS ARE INCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR EXTRUSIONS.
- D) DIMENSIONING AND TOLERANCING PER ASME Y14.5M 2009.
- E) DRAWING FILE NAME: MA03DREV11







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Definition of Terms				
Datasheet Identification	Product Status	Definition		
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No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.		
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