Preferred Devices

Dual General Purpose Transistors

PNP Duals

These transistors are designed for general purpose amplifier applications. They are housed in the SOT-363/SC-88 which is designed for low power surface mount applications.

• Device Marking:

BC856BDW1T1 = 3B

BC857BDW1T1 = 3F

BC857CDW1T1 = 3G

BC858BDW1T1 = 3K

BC858CDW1T1 = 3L

MAXIMUM RATINGS

Rating	Symbol	BC856	BC857	BC858	Unit
Collector-Emitter Voltage	V _{CEO}	-65	-45	-30	V
Collector - Base Voltage	V _{CBO}	-80	-50	-30	V
Emitter-Base Voltage	V _{EBO}	-5.0	-5.0	-5.0	V
Collector Current – Continuous	I _C	-100	-100	-100	mAdc

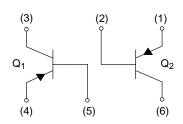
THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation Per Device FR-5 Board (Note 1) $T_{\Delta} = 25^{\circ}\text{C}$	P _D	380 250	mW
Derate Above 25°C		3.0	mW/°C
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	328	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C

1. $FR-5 = 1.0 \times 0.75 \times 0.062$ in



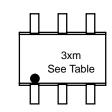
http://onsemi.com



DEVICE MARKING



SOT-363/SC-88 CASE 419B Style 1



3x = Specific Device Code

x = B, F, G, K, LM = Date Code

ORDERING INFORMATION

Device	Package	Shipping [†]
BC856BDW1T1	SOT-363	3000 Units/Reel
BC857BDW1T1	SOT-363	3000 Units/Reel
BC857CDW1T1	SOT-363	3000 Units/Reel
BC858BDW1T1	SOT-363	3000 Units/Reel
BC858CDW1T1	SOT-363	3000 Units/Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Charac	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS		<u>. </u>				
Collector – Emitter Breakdown Voltage (I _C = –10 mA)	V _(BR) CEO	-65 -45 -30	- - -	- - -	V	
Collector – Emitter Breakdown Voltage ($I_C = -10 \mu A$, $V_{EB} = 0$)	BC856 Series BC857B Only BC858 Series	V _(BR) CES	-80 -50 -30	- - -	- - -	V
Collector – Base Breakdown Voltage ($I_C = -10 \mu A$)	V _(BR) CBO	-80 -50 -30	- - -	- - -	V	
Emitter – Base Breakdown Voltage ($I_E = -1.0 \mu A$)	BC856 Series BC857 Series BC858 Series	V _{(BR)EBO}	-5.0 -5.0 -5.0	- - -	- - -	V
Collector Cutoff Current ($V_{CB} = -30 \text{ V}$) ($V_{CB} = -30 \text{ V}$,	I _{CBO}	- -	_ _	-15 -4.0	nA μA	
ON CHARACTERISTICS				I	1	1
(0 , 52 ,	856B, BC857B, BC858B 857C, BC858C	h _{FE}	- -	150 270	- -	-
$(I_C = -2.0 \text{ mA}, V_{CE} = -5.0 \text{ V})$ BC BC		220 420	290 520	475 800		
Collector – Emitter Saturation Voltage ($I_C = -10$ mA, $I_B = -0.5$ mA) ($I_C = -100$ mA, $I_B = -5.0$ mA)		V _{CE(sat)}	- -	_ _	-0.3 -0.65	V
Base – Emitter Saturation Voltage ($I_C = -10$ mA, $I_B = -0.5$ mA) ($I_C = -100$ mA, $I_B = -5.0$ mA)		V _{BE(sat)}		-0.7 -0.9	- -	V
Base – Emitter On Voltage (I_C = -2.0 mA, V_{CE} = -5.0 V) (I_C = -10 mA, V_{CE} = -5.0 V)	V _{BE(on)}	-0.6 -	_ _	-0.75 -0.82	V	
SMALL-SIGNAL CHARACTERISTICS	3					
Current – Gain – Bandwidth Product $(I_C = -10 \text{ mA}, V_{CE} = -5.0 \text{ Vdc}, f = 10 \text{ m})$	00 MHz)	fτ	100	_	_	MHz
Output Capacitance (V _{CB} = -10 V, f = 1.0 MHz)		C _{ob}	-	-	4.5	pF
Noise Figure ($I_C = -0.2 \text{ mA}, V_{CE} = -5.0 \text{ Vdc}, R_S = 6$ f = 1.0 kHz, BW = 200 Hz)	- 2.0 kΩ,	NF	-	_	10	dB

TYPICAL CHARACTERISTICS - BC856

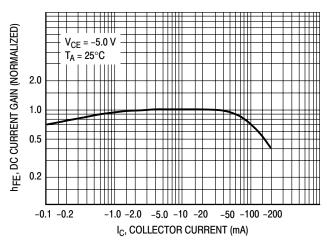


Figure 1. DC Current Gain

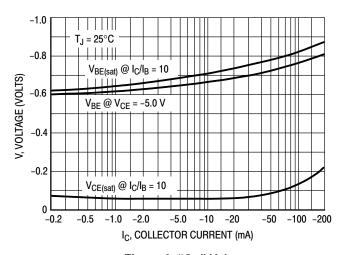


Figure 2. "On" Voltage

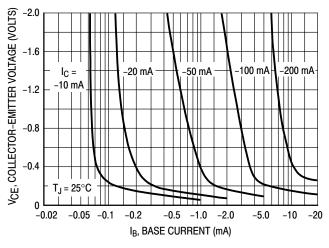


Figure 3. Collector Saturation Region

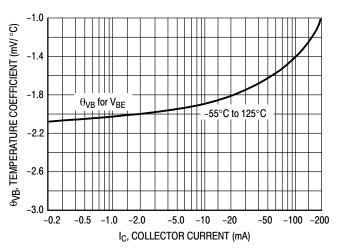


Figure 4. Base-Emitter Temperature Coefficient

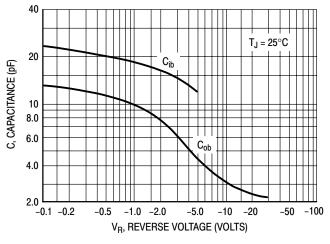


Figure 5. Capacitance

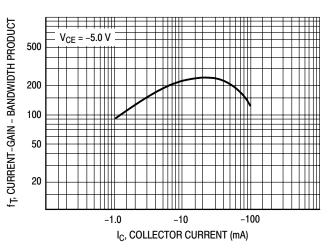


Figure 6. Current-Gain - Bandwidth Product

TYPICAL CHARACTERISTICS - BC857/BC858

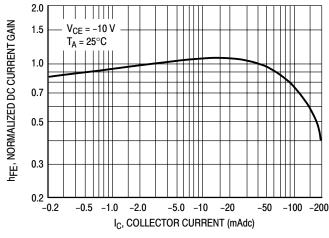


Figure 7. Normalized DC Current Gain

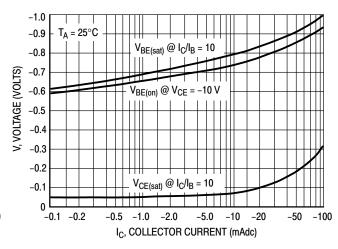


Figure 8. "Saturation" and "On" Voltages

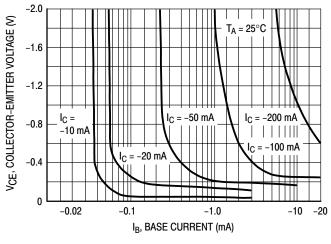


Figure 9. Collector Saturation Region

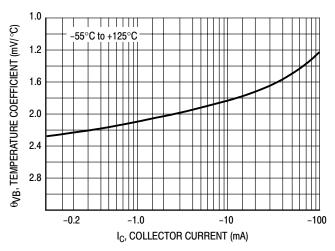


Figure 10. Base–Emitter Temperature Coefficient

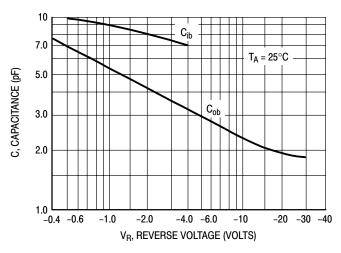


Figure 11. Capacitances

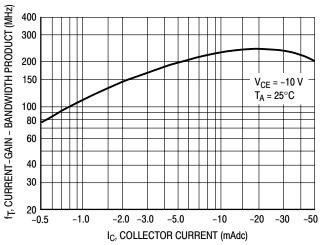


Figure 12. Current-Gain - Bandwidth Product

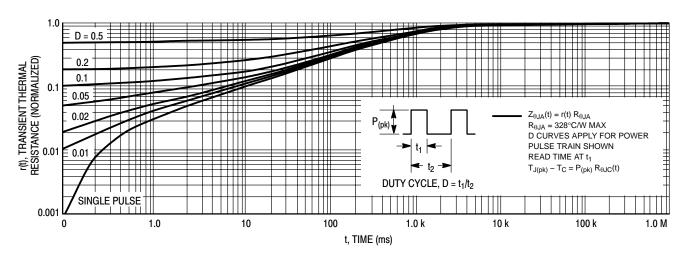


Figure 13. Thermal Response

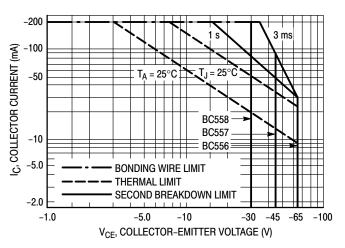


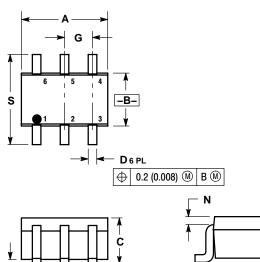
Figure 14. Active Region Safe Operating Area

The safe operating area curves indicate I_C – V_{CE} limits of the transistor that must be observed for reliable operation. Collector load lines for specific circuits must fall below the limits indicated by the applicable curve.

The data of Figure 14 is based upon $T_{J(pk)} = 150^{\circ}C$; T_{C} or T_{A} is variable depending upon conditions. Pulse curves are valid for duty cycles to 10% provided $T_{J(pk)} \leq 150^{\circ}C$. $T_{J(pk)}$ may be calculated from the data in Figure 13. At high case or ambient temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by the secondary breakdown.

PACKAGE DIMENSIONS

SC-88 (SOT-363) CASE 419B-02 **ISSUE T**



NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

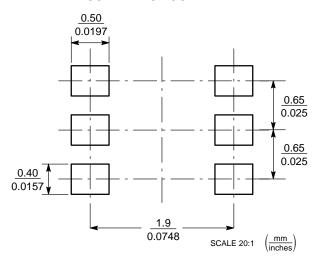
- 2. CONTROLLING DIMENSION: INCH.
 3. 419B-01 OBSOLETE, NEW STANDARD 419B-02.

	INCHES		MILLIMETER		
DIM	MIN	MAX	MIN	MAX	
Α	0.071	0.087	1.80	2.20	
В	0.045	0.053	1.15	1.35	
С	0.031	0.043	0.80	1.10	
D	0.004	0.012	0.10	0.30	
G	0.026 BSC		0.65 BSC		
Н		0.004		0.10	
J	0.004	0.010	0.10	0.25	
K	0.004	0.012	0.10	0.30	
N	0.008 REF		0.20 REF		
S	0.079	0.087	2.00	2.20	

STYLE 1:

- PIN 1. EMITTER 2 2. BASE 2 3. COLLECTOR 1
 - 4. EMITTER 1
 - 5 BASE 1
 - 6. COLLECTOR 2

SOLDERING FOOTPRINT*



SC-88/SC70-6

ON Semiconductor and was are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free

Japan: ON Semiconductor, Japan Customer Focus Center 2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051 Phone: 81-3-5773-3850

ON Semiconductor Website: http://onsemi.com

Order Literature: http://www.onsemi.com/litorder

For additional information, please contact your local Sales Representative.