

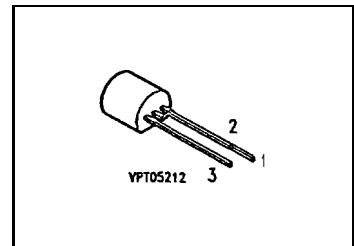
SIEMENS

PNP Silicon Darlington Transistors

BC 876

... BC 880

- High current gain
- High collector current
- Low collector-emitter Saturation voltage
- Complementary types: BC 875, BC 877,
BC 879 (NPN)



Type	Marking	Ordering Code	Pin Configuration	Package ¹⁾
			1 2 3	
BC876	-	C62702-C943	E	TO-92
BC 878		C62702-C942	C	
BC880	.	C62702-C941	B	

Maximum Ratings

Parameter	Symbol	Values	BC 876	BC 878	BC 880	Unit
Collector-emitter voltage	V _{CE0}	45	60	80		V
Collector-base voltage	V _{CBO}	60	80	100		
Emitter-base voltage	V _{EBO}			5		
Collector current	I _C			1		A
Peak collector current	I _{CM}			2		
Base current	I _B			100		mA'
Peak base current	I _{BM}			200		
Total power dissipation. $T_c = 90^\circ\text{C}$ ²⁾	P _{tot}			0.8 (1)		W
Junction temperature	T _j			150		°C
Storage temperature range	T _{stg}			-65 . . . + 150		

Thermal Resistance

Junction - ambient ²⁾	R _{thJA}	≤ 156	K/W
Junction - case ³⁾	R _{thJC}	≤ 75	

¹⁾ For detailed information see chapter Package Outlines.

²⁾ If transistors with max. 4 mm lead length are fixed on PCBs with a min. 10 mm × 10 mm large copper area for the collector terminal, R_{thJA} = 125 K/W and thus P_{tot max} = 1 W at T_A = 25 °C.

³⁾ Mounted on Al heat sink 15 mm × 25 mm × 0.5 mm.

Electrical Characteristicsat $T_A = 25^\circ\text{C}$, unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

DC characteristics

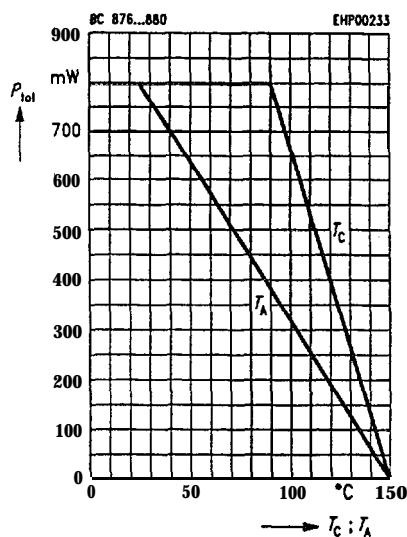
Collector-emitter breakdown voltage $I_C = 50 \text{ mA}$	$V_{(BR)CEO}$	45	—	—	V
BC 876		60	—	—	
BC 878		80	—	—	
Collector-base breakdown voltage $I_C = 100 \mu\text{A}$	$V_{(BR)CBO}$	60	—	—	
BC 876		80	—	—	
BC 880		100	—	—	
Emitter-base breakdown voltage, $I_E = 100 \mu\text{A}$	$V_{(BR)EBO}$	5	—	—	
Collector cutoff current $V_{CE} = 0.5 \times V_{CE\max}$	I_{CEO}	—	—	500	
Collector cutoff current $V_{CB} = V_{CB\max}$ $V_{CB} = V_{CB\max}, T_A = 150^\circ\text{C}$	I_{CBO}	—	100	—	μA
—		—	20	—	
Emitter cutoff current, $V_{EB} = 4 \text{ V}$	I_{EBO}	—	100	—	nA
DC current gain $I_C = 150 \text{ mA}; V_{CE} = 10 \text{ V}^1)$ $I_C = 500 \text{ mA}; V_{CE} = 10 \text{ V}^1)$	h_{FE}	1000	—	—	—
		2000 ¹⁾	—	—	
Collector-emitter Saturation voltage ¹⁾ $I_C = 500 \text{ mA}, I_B = 0.5 \text{ mA}$ $I_C = 1000 \text{ mA}, I_B = 1 \text{ mA}$	V_{CESat}	—	—	1.3	V
		—	—	1.8	
Base-emitter Saturation voltage ¹⁾ $I_C = 1000 \text{ mA}; I_B = 1 \text{ mA}$	V_{BESat}	—	—	2.2	

AC characteristics

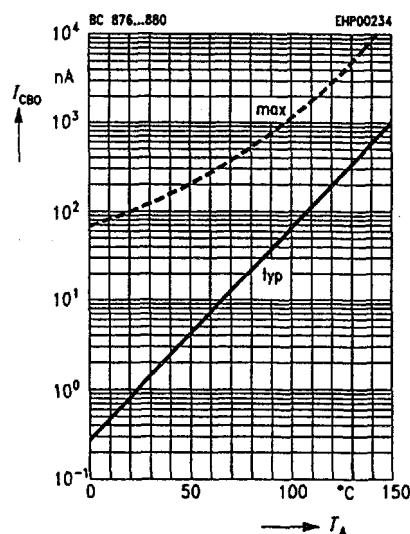
Transition frequency $I_C = 200 \text{ mA}, V_{CE} = 5 \text{ V}, f = 20 \text{ MHz}$	f_T	150	—	MHz
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¹⁾ Pulse test: $t \leq 300 \text{ ps}$. $D \leq 2 \%$.

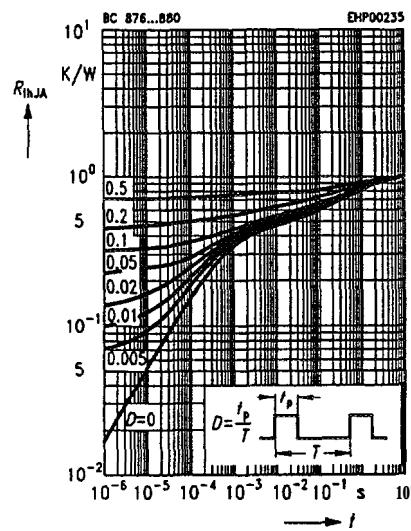
Total power dissipation $P_{\text{tot}} = f(T_A; T_C)$



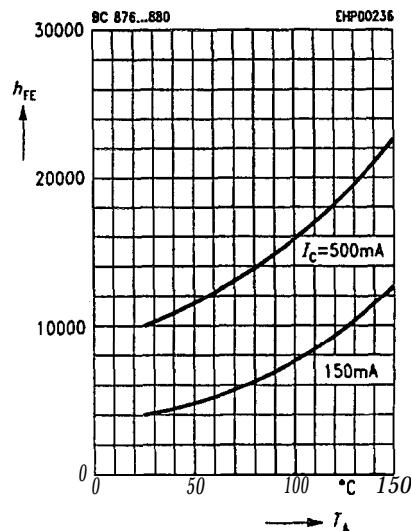
Collector cutoff current $I_{\text{CEO}} = f(T_A)$
 $V_{\text{CB}} = 100 \text{ V}$



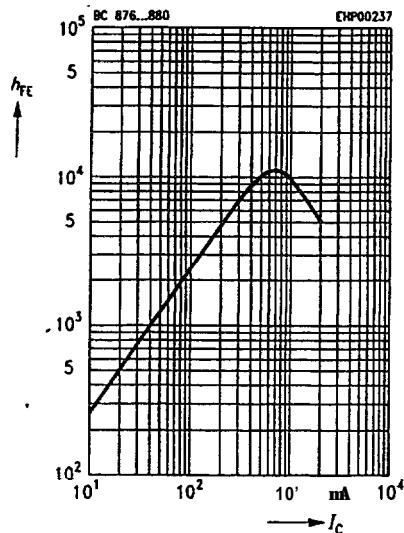
Permissible pulse load $R_{\text{thJA}} = f(t_p)$



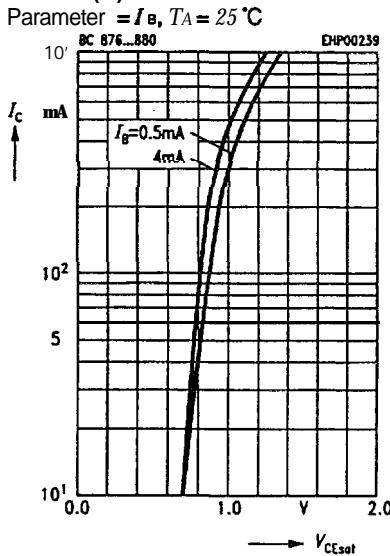
DC current gain $h_{\text{FE}} = f(T_A)$
 $V_{\text{CE}} = 10 \text{ V}$



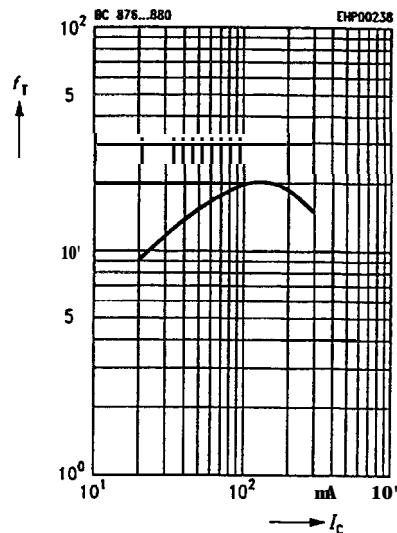
DC current gain $h_{FE} = f(I_C)$
 $V_{CE} = 10 \text{ V}$, $T_A = 25^\circ\text{C}$



Collector-emitter Saturation voltage
 $V_{CEsat} = f(I_C)$



Transition frequency $f_T = f(I_C)$
 $V_{CE} = 5 \text{ V}$, $f = 20 \text{ MHz}$



Base-emitter Saturation voltage
 $V_{BEsat} = f(I_C)$

