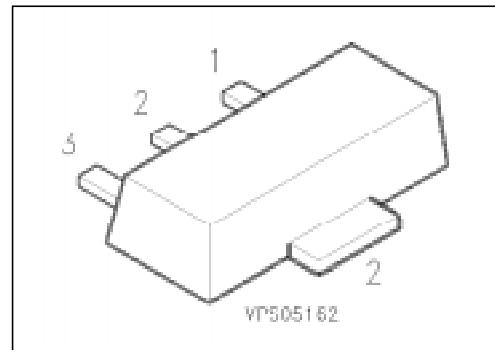


NPN Silicon Darlington Transistors

**BCV 29
BCV 49**

- For general AF applications
- High collector current
- High current gain
- Complementary types: BCV 28, BCV 48 (PNP)



Type	Marking	Ordering Code (tape and reel)	Pin Configuration				Package ¹⁾
			1	2	3	4	
BCV 29	EF	Q62702-C1853	B	C	E	C	SOT-89
BCV 49	EG	Q62702-C1832					

Maximum Ratings

Parameter	Symbol	Values		Unit
		BCV 29	BCV 49	
Collector-emitter voltage	V_{CE0}	30	60	V
Collector-base voltage	V_{CB0}	40	80	
Emitter-base voltage	V_{EB0}	10	10	
Collector current	I_C	500		
Peak collector current	I_{CM}	800		
Base current	I_B	100		
Peak base current	I_{BM}	200		
Total power dissipation, $T_S = 130^\circ\text{C}$	PI_{tot}	1		
Junction temperature	T_j	150		
Storage temperature range	T_{stg}	$-65 \dots +150$		

Thermal Resistance

Junction - ambient ²⁾	$R_{th JA}$	≤ 75	K/W
Junction - soldering point	$R_{th JS}$	≤ 20	

¹⁾ For detailed information see chapter Package Outlines.

²⁾ Package mounted on epoxy pcb 40 mm \times 40 mm \times 1.5 mm/6 cm² Cu.

Electrical Characteristicsat $T_A = 25^\circ\text{C}$, unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

DC characteristics

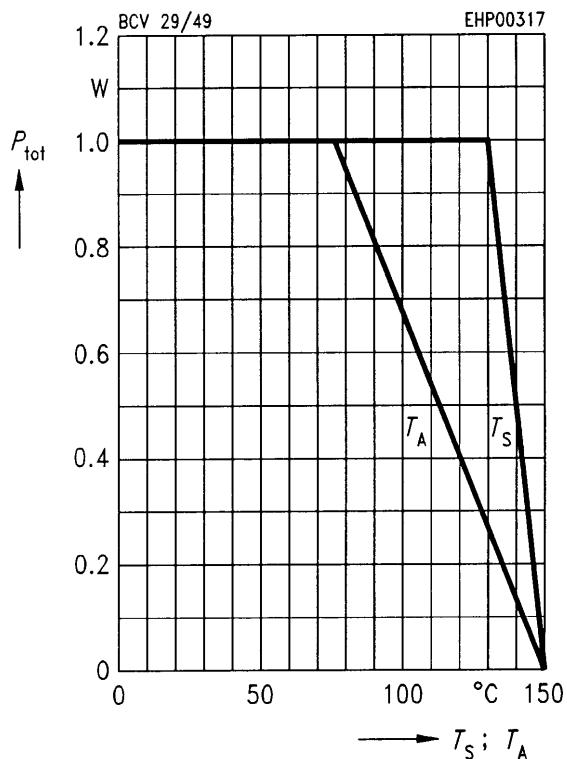
Collector-emitter breakdown voltage $I_C = 10 \text{ mA}$	$V_{(\text{BR})\text{CE}0}$	30 60	— —	— —	V
Collector-base breakdown voltage $I_C = 100 \mu\text{A}$	$V_{(\text{BR})\text{CB}0}$	40 80	— —	— —	
Emitter-base breakdown voltage, $I_E = 10 \mu\text{A}$	$V_{(\text{BR})\text{EB}0}$	10	—	—	
Collector cutoff current $V_{\text{CB}} = 30 \text{ V}$ $V_{\text{CB}} = 60 \text{ V}$ $V_{\text{CB}} = 30 \text{ V}, T_A = 150^\circ\text{C}$ $V_{\text{CB}} = 60 \text{ V}, T_A = 150^\circ\text{C}$	I_{CBO}	— — — —	— — — —	100 100 10 10	nA nA μA μA
Emitter cutoff current, $V_{\text{EB}} = 4 \text{ V}$	I_{EBO}	—	—	100	nA
DC current gain ¹⁾ $I_C = 100 \mu\text{A}, V_{\text{CE}} = 1 \text{ V}$ $I_C = 10 \text{ mA}, V_{\text{CE}} = 5 \text{ V}$ $I_C = 100 \text{ mA}, V_{\text{CE}} = 5 \text{ V}$ $I_C = 0.5 \text{ A}, V_{\text{CE}} = 5 \text{ V}$	h_{FE}	4000 2000 10000 4000 20000 10000 4000 2000	— — — — — — — —	— — — — — — — —	—
Collector-emitter saturation voltage ¹⁾ $I_C = 100 \text{ mA}, I_B = 0.1 \text{ mA}$	V_{CEsat}	—	—	1	V
Base-emitter saturation voltage ¹⁾ $I_C = 100 \text{ mA}; I_B = 0.1 \text{ mA}$	V_{BEsat}	—	—	1.5	

AC characteristics

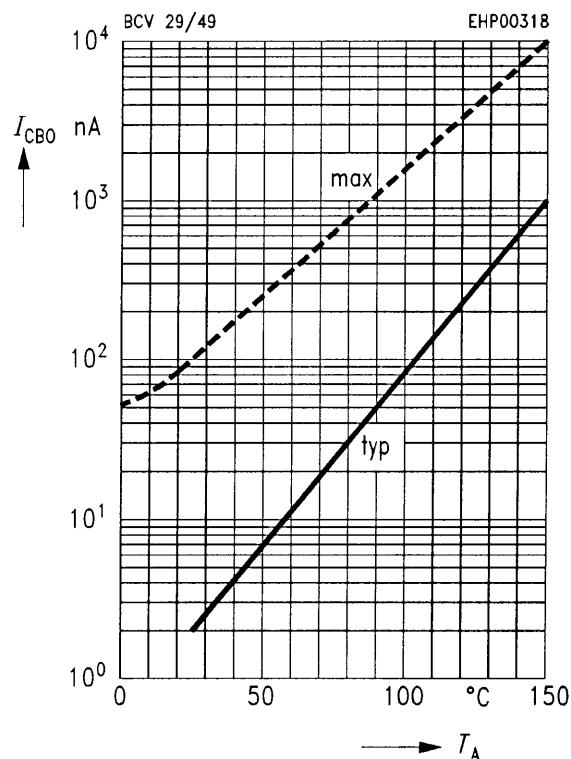
Transition frequency $I_C = 50 \text{ mA}, V_{\text{CE}} = 5 \text{ V}, f = 20 \text{ MHz}$	f	—	150	—	MHz
Output capacitance $V_{\text{CB}} = 10 \text{ V}, f = 1 \text{ MHz}$	C_{obo}	—	3.5	—	pF

¹⁾ Pulse test: $t \leq 300 \mu\text{s}$, $D = 2 \%$.

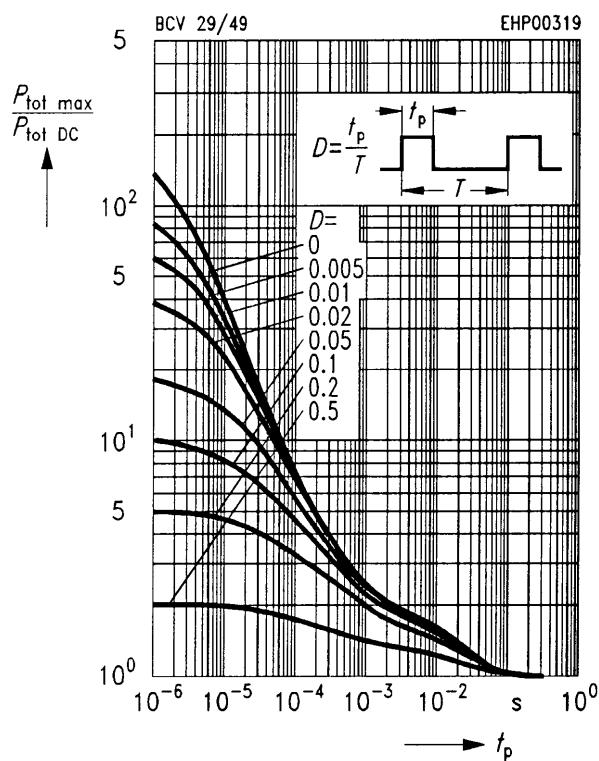
Total power dissipation $P_{\text{tot}} = f(T_A^*; T_S)$
 * Package mounted on epoxy



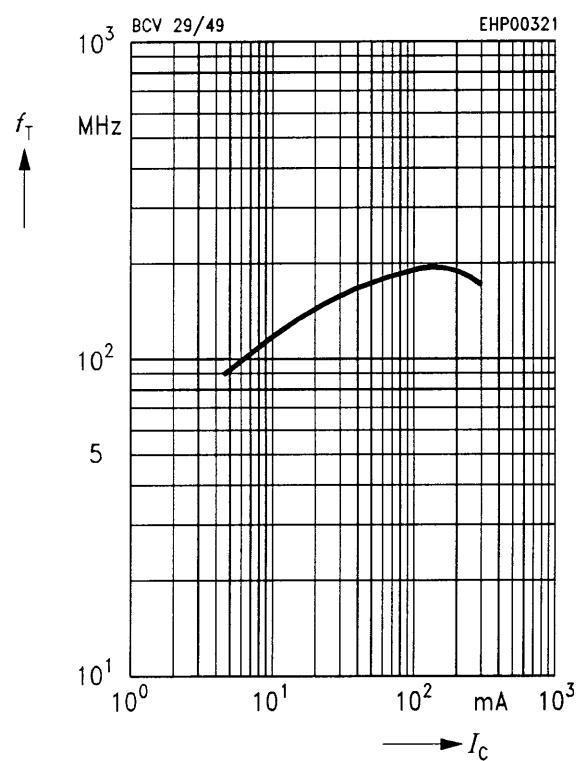
Collector cutoff current $I_{\text{CBO}} = f(T_A)$



Permissible pulse load $P_{\text{tot max}}/P_{\text{tot DC}} = f(t_p)$



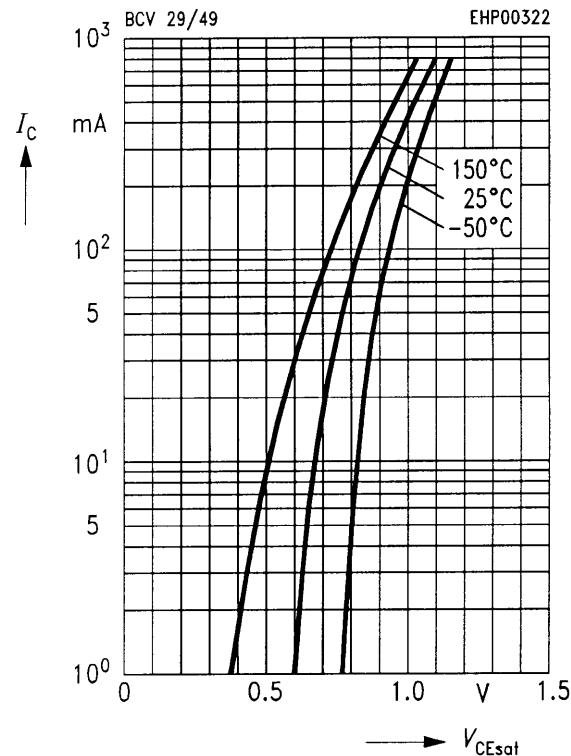
Transition frequency $f_T = f(I_C)$
 $V_{\text{CE}} = 5$ V



Collector-emitter saturation voltage

$$I_C = f(V_{CEsat})$$

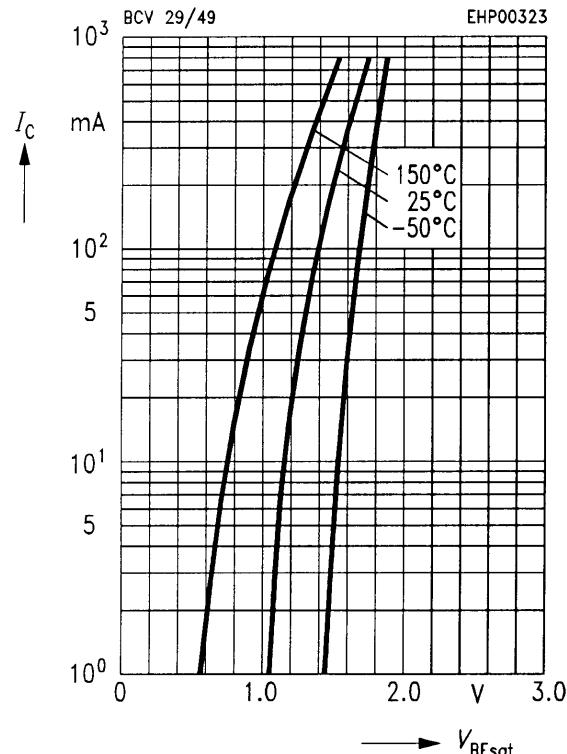
$$h_{FE} = 1000$$



Base-emitter saturation voltage

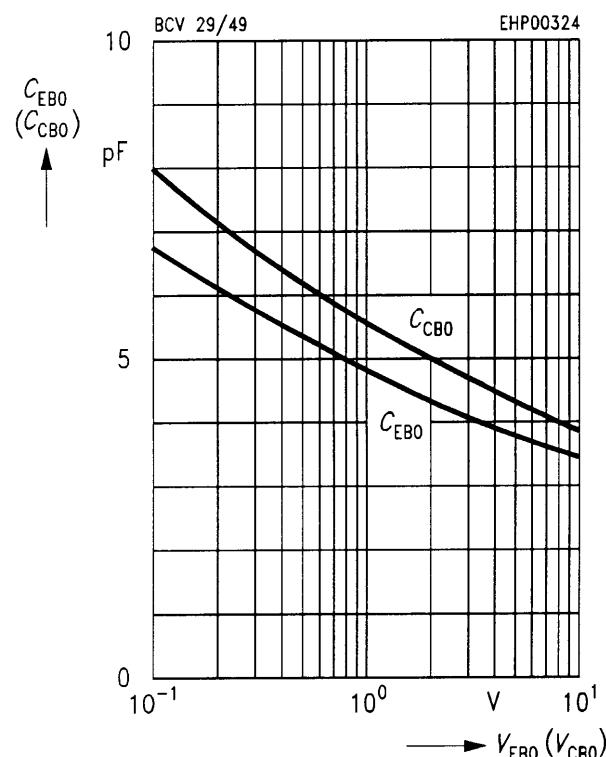
$$I_C = f(V_{BEsat})$$

$$h_{FE} = 1000$$



Collector-base capacitance $C_{CB0} = f(V_{CB0})$

Emitter-base capacitance $C_{EB0} = f(V_{EB0})$



DC current gain $h_{FE} = f(I_C)$

$$V_{CE} = 5 \text{ V}$$

