

## BD533 BD535 BD537 BD534 BD536

## Complementary power transistors

#### **Features**

■ BD533, BD535, and BD537 are NPN transistors

### **Description**

The devices are manufactured in Planar technology with "Base Island" layout. The resulting transistor shows exceptional high gain performance coupled with very low saturation voltage. The PNP types are BD534 and BD536.

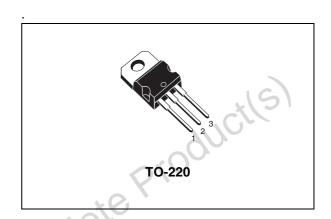


Figure 1. Internal schematic diagrams

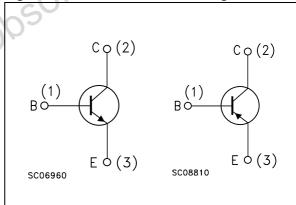


Table 1. Device summary

Order code	Marking	Package	Packaging
BD533	BD533		
BD534	BD534		
BD535	BD535	TO-220	Tube
BD536	BD536		
BD537	BD537		

July 2007 Rev 4 1/11

# 1 Absolute maximum ratings

Table 2. Absolute maximum ratings

Symbol	Parameter		Value			Unit
		NPN	BD533	BD535	BD537	
		PNP	BD534	BD536		
V <sub>CBO</sub>	Collector-base voltage (I <sub>E</sub> = 0)		45	60	80	V
V <sub>CES</sub>	Collector-emitter voltage (V <sub>B</sub> = 0)		45	60	80 -	V
V <sub>CEO</sub>	Collector-base voltage (I <sub>B</sub> = 0)		45	60	80	V
V <sub>EBO</sub>	Emitter-base voltage (I <sub>C</sub> = 0)			5	Cr	V
I <sub>C</sub>	Collector current			8	<i>)</i>	Α
I <sub>B</sub>	Base current		01	<b>O</b> 1		Α
P <sub>TOT</sub>	Total dissipation at T <sub>case</sub> = 25°C			50		W
T <sub>stg</sub>	Storage temperature	101	0	-65 to 150	)	°C
T <sub>J</sub>	Max. operating junction temperature	Olo		150		°C

Note: For PNP types voltage and current values are negative

Obsolete Product(s)

#### Table 3. Electrical characteristics

 $(T_{case} = 25^{\circ}C; unless otherwise specified)$ 

Table 4. Electrical characteristics

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I <sub>CBO</sub>	Collector cut-off current (I <sub>E</sub> = 0)	V <sub>CB</sub> = rated V <sub>CBO</sub>			0.1	mA
	Collector cut-off current (V <sub>BE</sub> = 0)	for BD533/534 V <sub>CE</sub> = 45 V			0.1	mA
I <sub>CES</sub>		for BD535/536 $V_{CE} = 60 \text{ V}$			0.1	mA
		for BD537 $V_{CE} = 80 \text{ V}$			0.1	mA
I <sub>EBO</sub>	Emitter cut-off current (I <sub>C</sub> = 0)	V <sub>EB</sub> = 5V		٠, (		mA
	Collector-emitter	I <sub>C</sub> = 100mA for BD533/534	45	10,		V
V <sub>CEO(sus)</sub> <sup>(1)</sup>	sustaining voltage	for BD535/536	60	<i></i>		V
	(I <sub>B</sub> = 0)	for BD537	80			٧
v (1)	Collector-emitter saturation voltage	$I_C = 2A$ $I_B = 0.2A$			0.8	٧
V <sub>CE(sat)</sub> <sup>(1)</sup>		$I_{C} = 6A$ $I_{B} = 0.6A$		0.8		V
V <sub>BE</sub> <sup>(1)</sup>	Base-emitter voltage	I <sub>C</sub> = 2A V <sub>CE</sub> = 2V			1.5	V
	(5)	$I_C = 10 \text{mA}$ $V_{CE} = 5 \text{V}$				
		for BD533/534	20			
		for BD535/536	20			
		for BD537	15			
h <sub>FE</sub> <sup>(1)</sup>	DC current gain	$I_C = 500 \text{mA}$ $V_{CE} = 2V$	40			
	90,	$I_C = 2A$ $V_{CE} = 2V$				
	5	for BD533/534				
		for BD535/536	25			
×6,		for BD537	15			

<sup>1.</sup> Pulsed duration = 300 ms, duty cycle ≥1.5%.

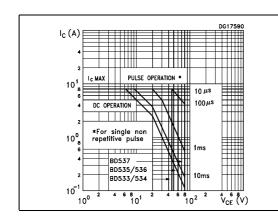
lote: For PNP types voltage e current values are negative.

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### 1.1 Electrical characteristic (curves)

Figure 2. Safe operating area

Figure 3. Derating curve



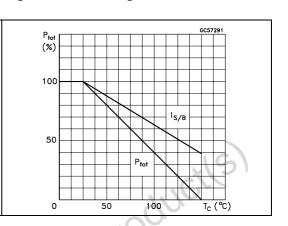
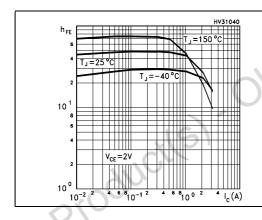


Figure 4. DC current gain (NPN)

Figure 5. DC current gain (PNP)



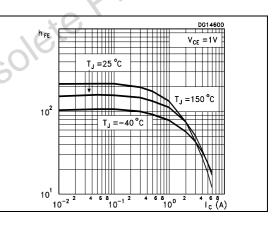
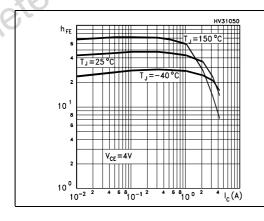


Figure 6. DC current gain (NPN)

Figure 7. DC current gain (PNP)



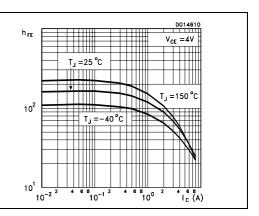
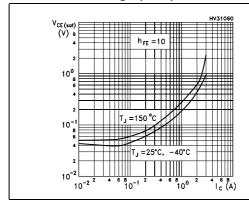


Figure 8. Collector-emitter saturation voltage (NPN)

Figure 9. Collector-emitter saturation voltage (PNP)



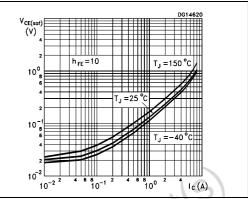
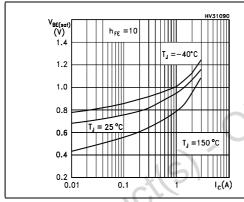


Figure 10. Base-emitter saturation voltage (NPN)

Figure 11. Base-emitter saturation voltage (PNP)



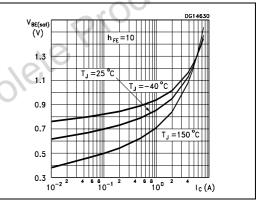
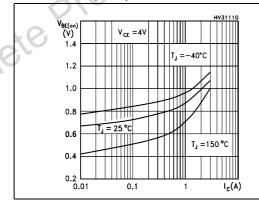


Figure 12. Base-emitter on voltage (NPN)

Figure 13. Base-emitter on voltage (PNP)



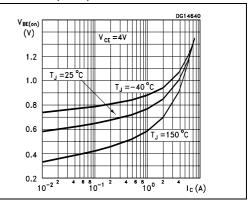


Figure 14. Resistive load switching time Figure 15. Resistive load switching time (NPN) (PNP)

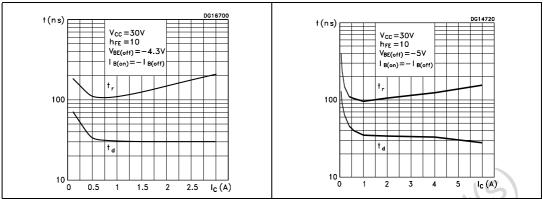


Figure 16. Resistive load switching time Figure 17. Resistive load switching time (NPN) (PNP)

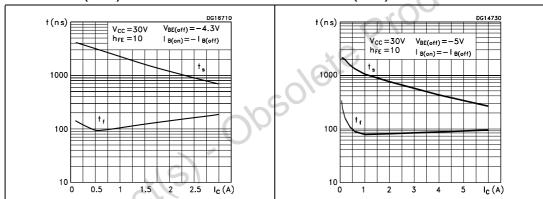
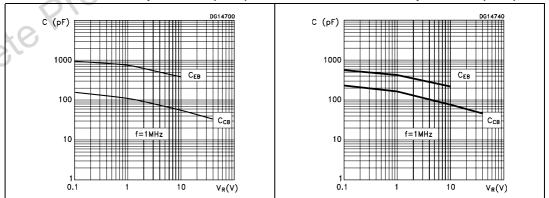
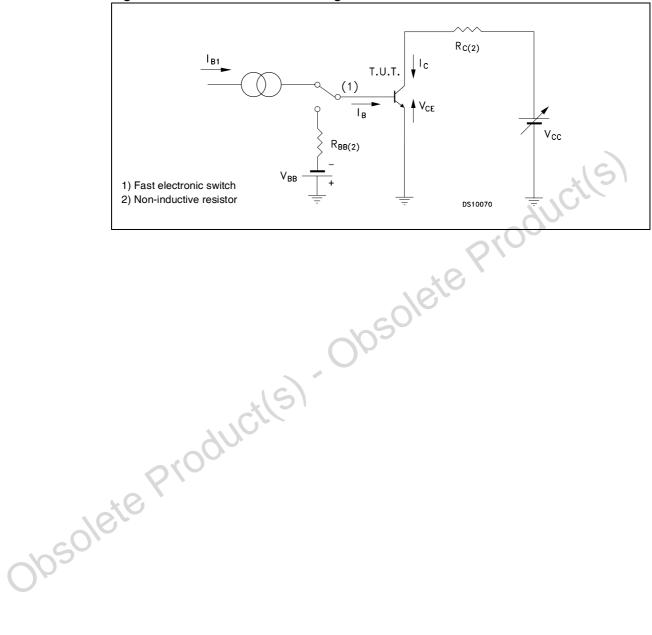


Figure 18. Collector-base and collector- Figure 19. Collector-base and collector- emitter capacitance (NPN) emitter capacitance (PNP)



### 1.2 Test circuits

Figure 20. Resistive load switching test circuit

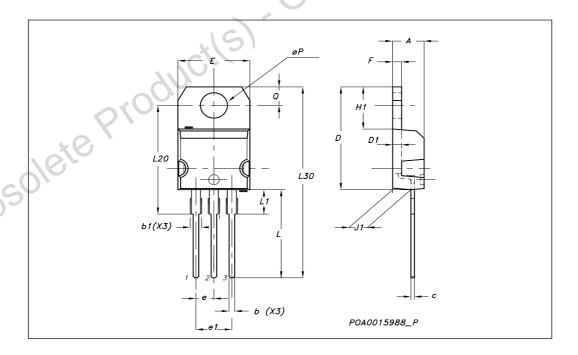


## 2 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available Obsolete Product(s). Obsolete Product(s) at: www.st.com

#### **TO-220 Mechanical data**

DIM.		mm.	
	MIN.	ТҮР	MAX.
Α	4.40		4.60
b	0.61		0.88
b1	1.14		1.70
С	0.49		0.70
D	15.25		15.75
D1		1.27	
E	10		10.40
е	2.40		2.70
e1	4.95		5.15
F	1.23		1.32
H1	6.20		6.60
J1	2.40		2.72
L	13		14
L1	3.50		3.93
L20		16.40	
L30		28.90	
øΡ	3.75	105	3.85
Q	2.65		2.95



## 3 Revision history

Table 5. Revision history

	Date	Revision	Changes		
	01-Jun-1997	1	Initial Release		
	11-Feb-2003	2	Minor text changes		
	27-Mar-2007	3	Figure 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19 and figure 20 added		
	23-Jul-2007	4	Figure 2 and figure 3 added		
figure 20 added 23-Jul-2007 4 Figure 2 and figure 3 added					

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