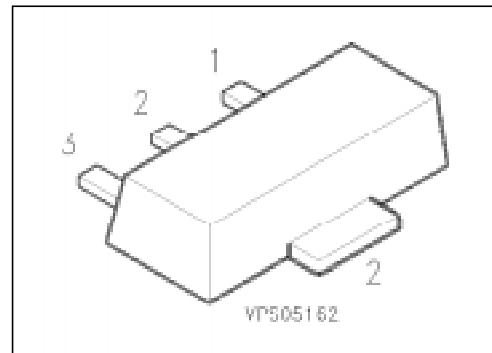


NPN Silicon High-Voltage Transistors

**BFN 16
BFN 18**

- Suitable for video output stages in TV sets and switching power supplies
- High breakdown voltage
- Low collector-emitter saturation voltage
- Complementary types: BFN 17, BFN 19 (PNP)



Type	Marking	Ordering Code (tape and reel)	Pin Configuration			Package ¹⁾
			1	2	3	
BFN 16	DD	Q62702-F885	B	C	E	SOT-89
BFN 18	DE	Q62702-F1056				

Maximum Ratings

Parameter	Symbol	Values		Unit
		BFN 16	BFN 18	
Collector-emitter voltage	V_{CEO}	250	300	V
Collector-base voltage	V_{CBO}	250	300	
Emitter-base voltage	V_{EBO}		5	
Collector current	I_C	200		mA
Peak collector current	I_{CM}		500	
Base current	I_B		100	
Peak base current	I_{BM}		200	
Total power dissipation, $T_S = 130^\circ\text{C}$	P_{tot}		1	W
Junction temperature	T_j		150	$^\circ\text{C}$
Storage temperature range	T_{stg}	− 65 ... + 150		

Thermal Resistance

Junction - ambient ²⁾	$R_{th JA}$	≤ 75	K/W
Junction - soldering point	$R_{th JS}$	≤ 20	

¹⁾ For detailed information see chapter Package Outlines.

²⁾ Package mounted on epoxy pcb 40 mm × 40 mm × 1.5 mm/6 cm² Cu.

Electrical Characteristicsat $T_A = 25^\circ\text{C}$, unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

DC characteristics

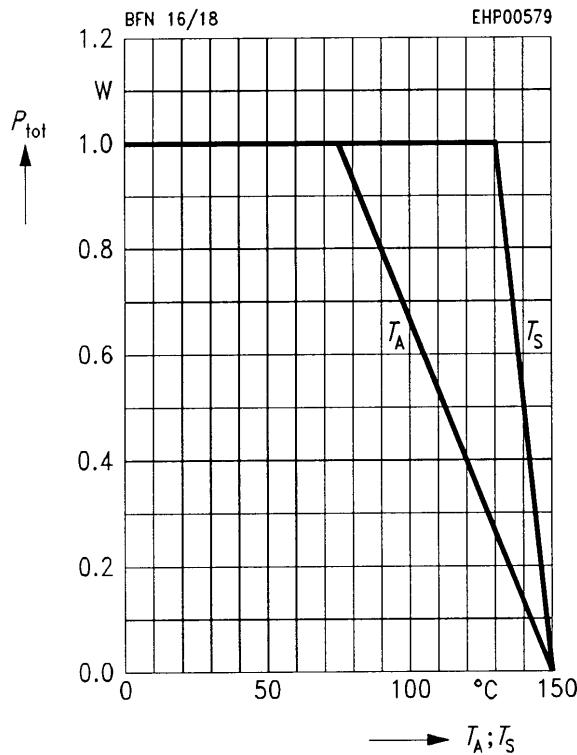
Collector-emitter breakdown voltage $I_C = 1 \text{ mA}$	$V_{(\text{BR})\text{CE}0}$	250 300	— —	— —	V
Collector-base breakdown voltage $I_C = 100 \mu\text{A}$	$V_{(\text{BR})\text{CB}0}$	250 300	— —	— —	
Emitter-base breakdown voltage $I_E = 100 \mu\text{A}$	$V_{(\text{BR})\text{EB}0}$	5	—	—	
Collector-base cutoff current $V_{CB} = 200 \text{ V}$	I_{CB0}	—	—	100	nA
$V_{CB} = 250 \text{ V}$		—	—	100	nA
$V_{CB} = 200 \text{ V}, T_A = 150^\circ\text{C}$	I_{CB0}	—	—	20	μA
$V_{CB} = 250 \text{ V}, T_A = 150^\circ\text{C}$	I_{CB0}	—	—	20	μA
Emitter-base cutoff current $V_{EB} = 3 \text{ V}$	I_{EB0}	—	—	100	nA
DC current gain $I_C = 1 \text{ mA}, V_{CE} = 10 \text{ V}$	h_{FE}	25 40	— —	— —	—
$I_C = 10 \text{ mA}, V_{CE} = 10 \text{ V}^1)$		40	—	—	
$I_C = 30 \text{ mA}, V_{CE} = 10 \text{ V}^1)$	I_{CB0}	40 30	— —	— —	
Collector-emitter saturation voltage ¹⁾ $I_C = 20 \text{ mA}, I_B = 2 \text{ mA}$	$V_{CE\text{sat}}$	— —	— —	0.4 0.5	V
Base-emitter saturation voltage ¹⁾ $I_C = 20 \text{ mA}, I_B = 2 \text{ mA}$	$V_{BE\text{sat}}$	—	—	0.9	

AC characteristics

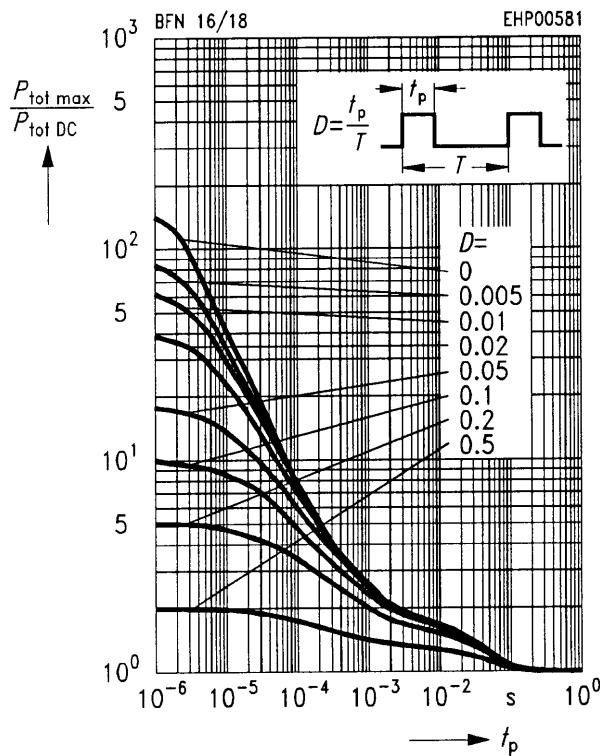
Transition frequency $I_C = 20 \text{ mA}, V_{CE} = 10 \text{ V}, f = 20 \text{ MHz}$	f	—	70	—	MHz
Output capacitance $V_{CB} = 30 \text{ V}, f = 1 \text{ MHz}$	C_{obo}	—	1.5	—	pF

¹⁾ Pulse test conditions: $t \leq 300 \mu\text{s}$, $D = 2\%$.

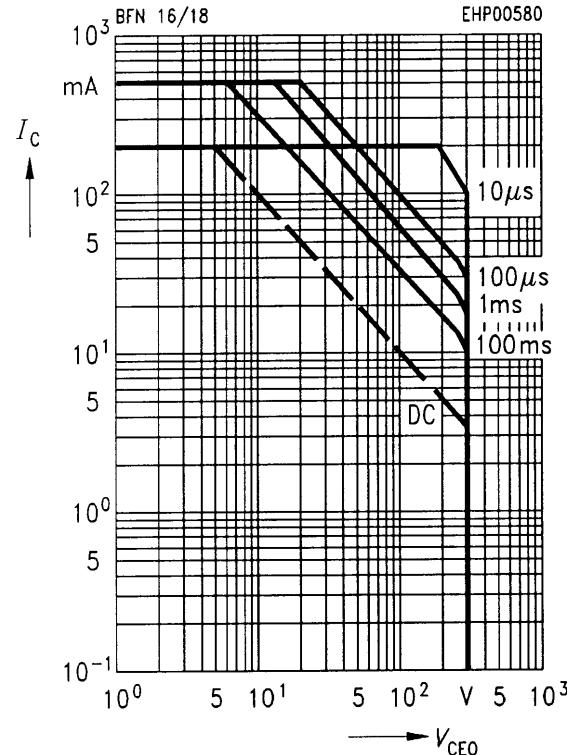
Total power dissipation $P_{\text{tot}} = f(T_A^*; T_S)$
 * Package mounted on epoxy



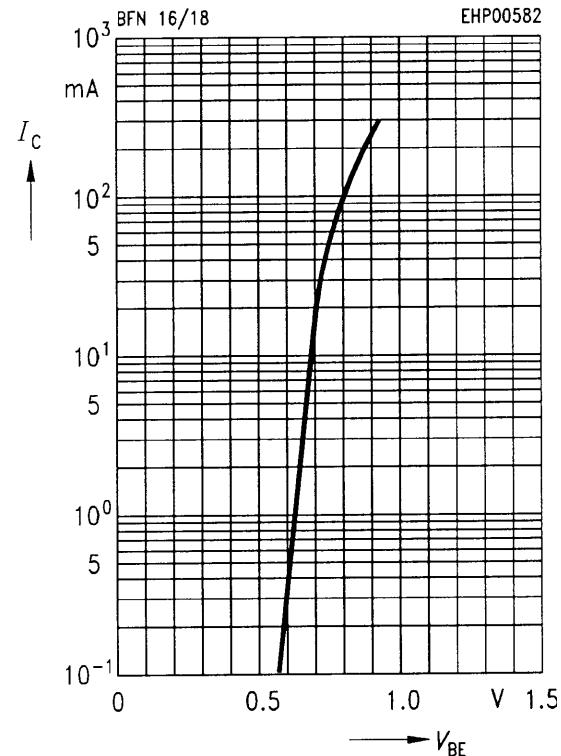
Permissible pulse load $P_{\text{tot max}}/P_{\text{tot DC}} = f(t_p)$



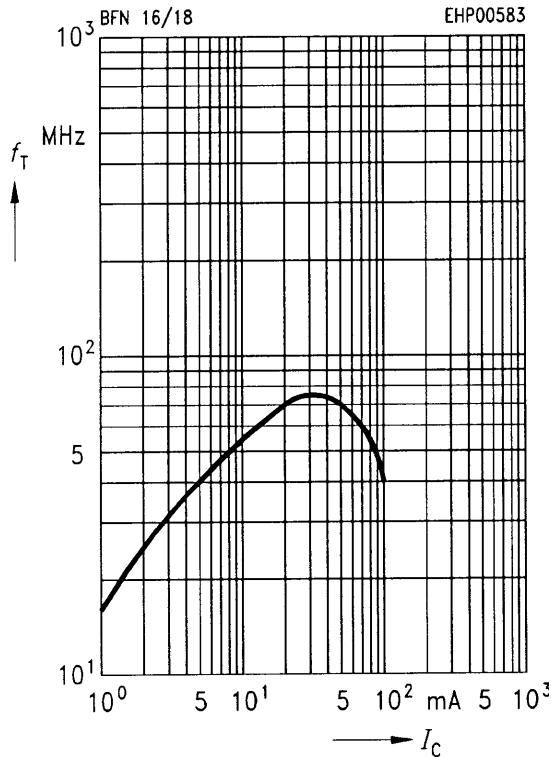
Operating range $I_C = f(V_{\text{CE}0})$
 $T_A = 25^{\circ}\text{C}, D = 0$



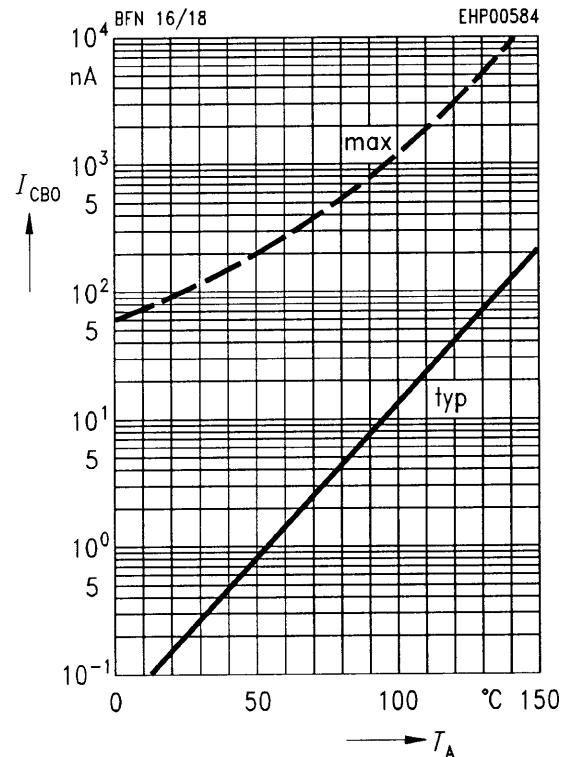
Collector current $I_C = f(V_{\text{BE}})$
 $V_{\text{CE}} = 10 \text{ V}$



Transition frequency $f_T = f(I_C)$
 $V_{CE} = 10 \text{ V}$



Collector cutoff current $I_{CBO} = f(T_A)$
 $V_{CB} = 200 \text{ V}$



DC current gain $h_{FE} = f(I_C)$
 $V_{CE} = 10 \text{ V}$

