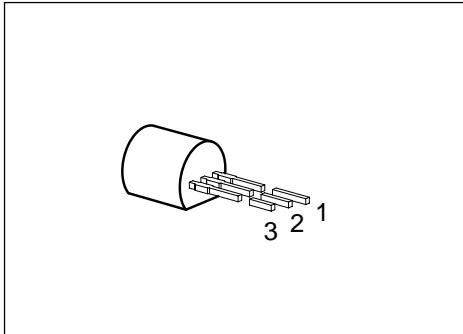


## NPN Silicon Transistors with High Reverse Voltage

BFP 22  
BFP 25

- High breakdown voltage
- Low collector-emitter saturation voltage
- Low capacitance
- Complementary types: BFP 23, BFP 26 (PNP)



Type	Marking	Ordering Code (tape and reel)	Pin Configuration			Package <sup>1)</sup>
			1	2	3	
BFP 22	–	Q62702-F621	E	B	C	TO-92
BFP 25		Q62702-F721				

### Maximum Ratings

Parameter	Symbol	Values BFP 22	BFP 25	Unit	
Collector-emitter voltage	$V_{CE0}$	200	300	V	
Collector-base voltage	$V_{CB0}$	200	300		
Emitter-base voltage	$V_{EB0}$	6			
Collector current	$I_c$	200			
Peak collector current	$I_{CM}$	500		mA	
Base current	$I_B$	100			
Peak base current	$I_{BM}$	200			
Total power dissipation, $T_C = 66^\circ\text{C}$	$P_{tot}$	625			
Junction temperature	$T_j$	150		$^\circ\text{C}$	
Storage temperature range	$T_{stg}$	– 65 ... + 150			

### Thermal Resistance

Junction - ambient	$R_{th JA}$	$\leq 200$	K/W
Junction - case <sup>2)</sup>	$R_{th JC}$	$\leq 135$	

<sup>1)</sup> For detailed information see chapter Package Outlines.

<sup>2)</sup> Mounted on Al heat sink 15 mm × 25 mm × 0.5 mm.

### Electrical Characteristics

at  $T_A = 25^\circ\text{C}$ , unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

### DC characteristics

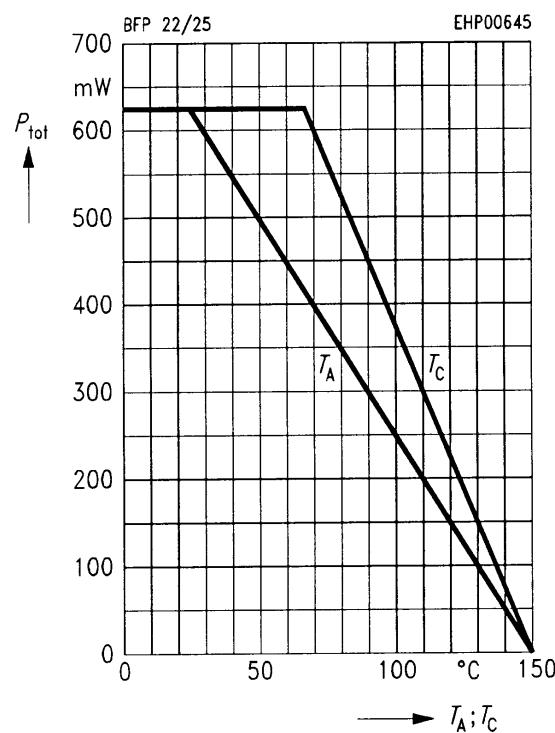
Collector-emitter breakdown voltage $I_C = 1 \text{ mA}$	$V_{(\text{BR})\text{CE}0}$	200 300	— —	— —	V
Collector-base breakdown voltage $I_C = 100 \mu\text{A}$	$V_{(\text{BR})\text{CB}0}$	200 300	— —	— —	
Emitter-base breakdown voltage $I_E = 100 \mu\text{A}$	$V_{(\text{BR})\text{EB}0}$	6	—	—	
Collector-base cutoff current $V_{CB} = 160 \text{ V}$	$I_{CB0}$	—	—	100	nA
$V_{CB} = 250 \text{ V}$		—	—	100	nA
$V_{CB} = 160 \text{ V}, T_A = 150^\circ\text{C}$	$I_{CB0}$	—	—	20	$\mu\text{A}$
$V_{CB} = 250 \text{ V}, T_A = 150^\circ\text{C}$	$I_{CB0}$	—	—	20	$\mu\text{A}$
Emitter-base cutoff current $V_{EB} = 4 \text{ V}$	$I_{EB0}$	—	—	100	nA
DC current gain $I_C = 1 \text{ mA}, V_{CE} = 10 \text{ V}$	$h_{FE}$	25 40 50 40	— — — —	— — — —	—
$I_C = 10 \text{ mA}, V_{CE} = 10 \text{ V}^1)$					
$I_C = 30 \text{ mA}, V_{CE} = 10 \text{ V}^1)$	$I_{CB0}$	—	—	0.4	
Collector-emitter saturation voltage <sup>1)</sup> $I_C = 20 \text{ mA}, I_B = 2 \text{ mA}$	$V_{CE\text{sat}}$	— —	— —	0.4 0.5	V
Base-emitter saturation voltage <sup>1)</sup> $I_C = 20 \text{ A}, I_B = 2 \text{ mA}$	$V_{BE\text{sat}}$	—	—	0.9	

### AC characteristics

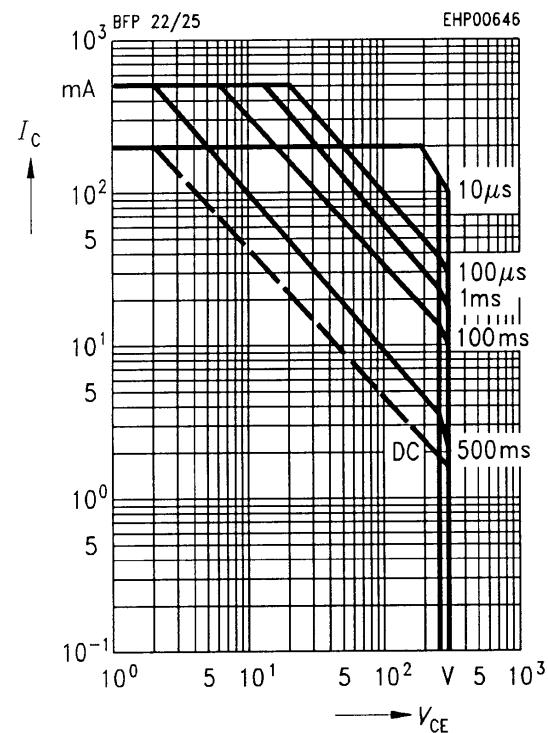
Transition frequency $I_C = 20 \text{ mA}, V_{CE} = 10 \text{ V}, f = 20 \text{ MHz}$	$f$	—	70	—	MHz
Output capacitance $V_{CB} = 30 \text{ V}, f = 1 \text{ MHz}$	$C_{\text{obo}}$	—	1.5	—	pF

<sup>1)</sup> Pulse test conditions:  $t \leq 300 \mu\text{s}$ ,  $D \leq 2 \%$ .

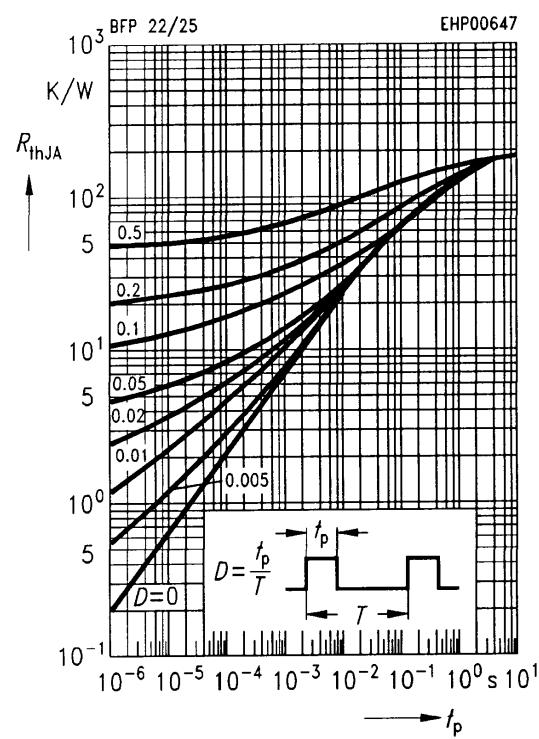
**Total power dissipation**  $P_{\text{tot}} = f(T_A; T_C)$



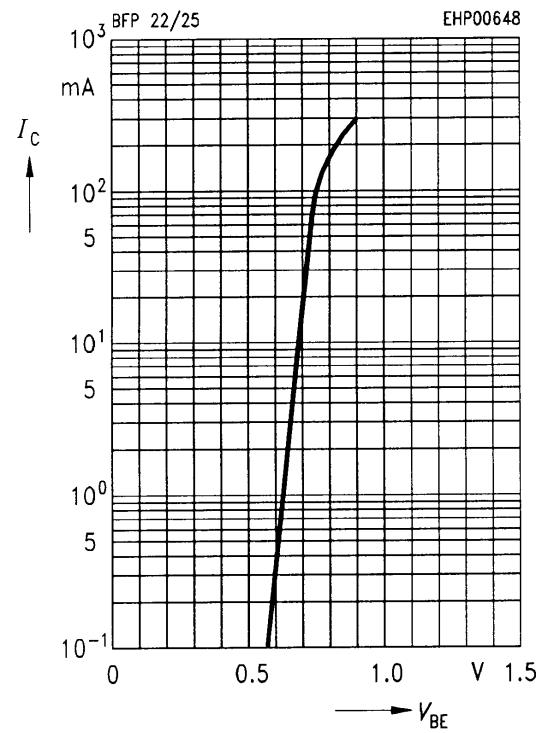
**Operating range**  $I_C = f(V_{CE})$   
 $D = 0, T_A = 25^\circ\text{C}$



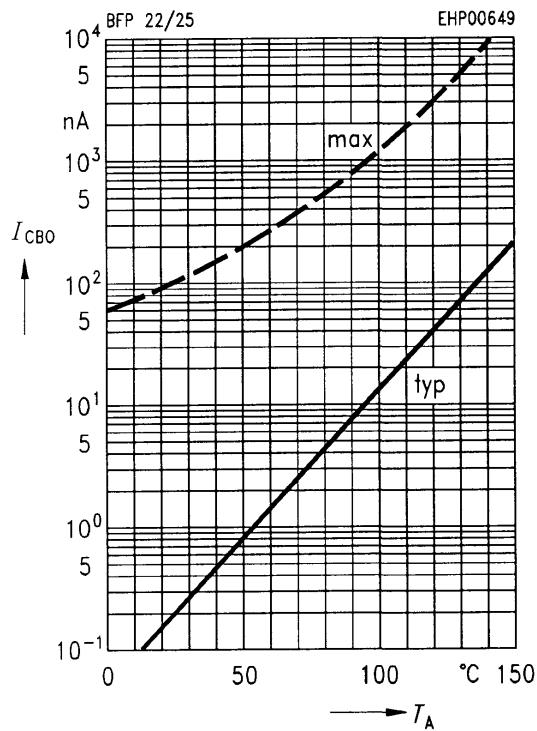
**Permissible pulse load**  $R_{\text{thJA}} = f(t_p)$



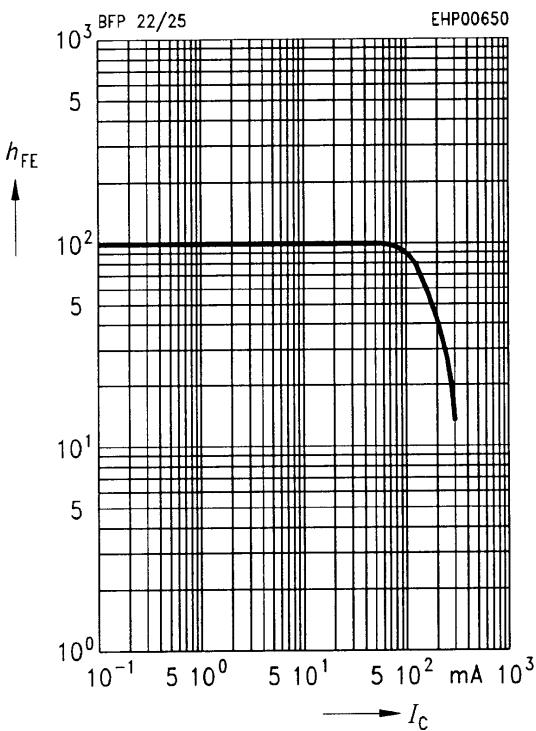
**Collector current**  $I_C = f(V_{BE})$   
 $V_{CE} = 10\text{ V}, T_A = 25^\circ\text{C}$



**Collector cutoff current**  $I_{CBO} = f(T)$   
 $V_{CB} = 160 \text{ V}, 250 \text{ V}$



**DC current gain**  $h_{FE} = f(I_C)$   
 $V_{CE} = 10 \text{ V}, T_A = 25 \text{ °C}$



**Transition frequency**  $f_T = f(I_C)$   
 $V_{CE} = 10 \text{ V}, f = 20 \text{ MHz}$

