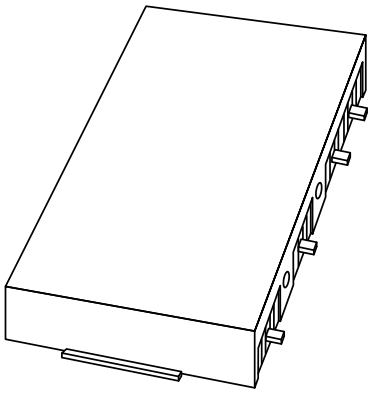
DATA SHEET



BGY206
UHF amplifier module

Product specification
Supersedes data of 1998 Apr 15

1998 May 08

Philips
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UHF amplifier module BGY206

FEATURES

- 4.8 V nominal supply voltage
- 3 W output power
- Easy control of output power by DC voltage.

APPLICATIONS

- Digital cellular radio systems with Time Division Multiple Access (TDMA) operation (GSM systems) in the 880 to 915 MHz frequency range.

DESCRIPTION

The BGY206 is a three-stage UHF amplifier module in a SOT388B package. The module consists of three NPN silicon planar transistor dies mounted together with matching and bias circuit components on a metallized ceramic substrate.

PINNING - SOT388B

PIN	DESCRIPTION
1	RF input
2	V_C
3	V_S
4	RF output
Flange	ground

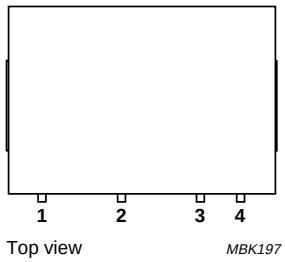


Fig.1 Simplified outline.

QUICK REFERENCE DATA

RF performance at T_{mb} = 25 °C.

MODE OF OPERATION	f (MHz)	V_S (V)	V_C (V)	P_L (W)	G_p (dB)	η (%)	Z_S; Z_L (Ω)
Pulsed; δ = 1 : 8	880 to 915	4.8	≤3.5	3	≥30	typ. 45	50

LIMITING VALUES

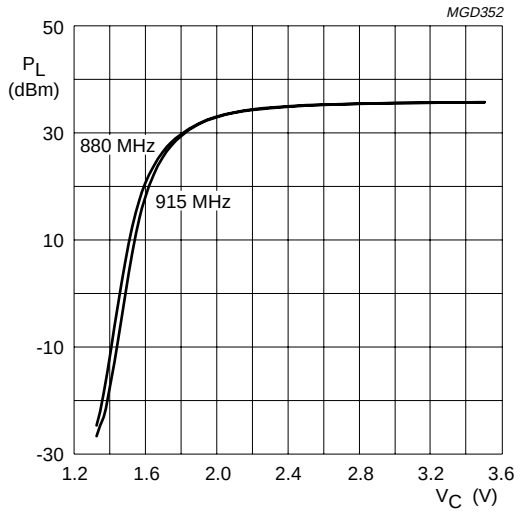
In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_S	DC supply voltage	V_C < 0.5 V	–	10	V
V_C	DC control voltage		–	4	V
P_D	input drive power		–	13	mW
P_L	load power		–	3.5	W
T_{stg}	storage temperature		–40	+100	°C
T_{mb}	operating mounting base temperature		–30	+100	°C

CHARACTERISTICS

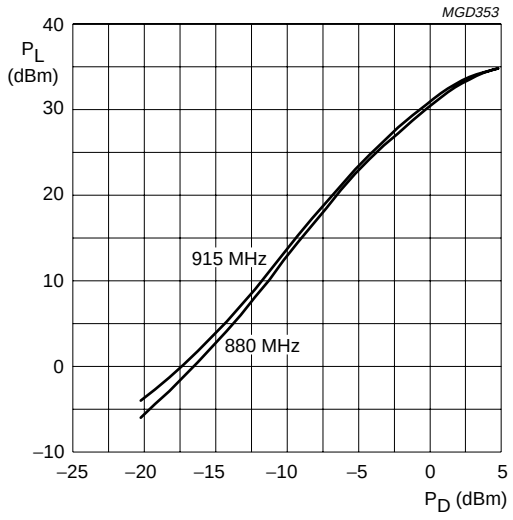
$Z_S = Z_L = 50\ \Omega$; $P_D = 3$ mW; $V_S = 4.8$ V; $V_C \leq 3.5$ V; f = 880 to 915 MHz; $T_{mb} = 25$ °C; $\delta = 1 : 8$; $t_p = 575\ \mu s$; unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I_Q	leakage current	$V_C = 0.5$ V	–	–	100	μA
I_C	control current	adjust V_C for $P_L = 3$ W	–	–	500	μA
P_L	load power	$V_C = 3.5$ V	3	–	–	W
		$V_C = 3.5$ V; $V_S = 4.3$ V; $T_{mb} = 85$ °C	2	–	–	W
G_p	power gain	adjust V_C for $P_L = 3$ W	30	–	–	dB
η	efficiency	adjust V_C for $P_L = 3$ W	40	45	–	%
H_2	second harmonic	adjust V_C for $P_L = 3$ W	–	–	–40	dBc
H_3	third harmonic	adjust V_C for $P_L = 3$ W	–	–	–40	dBc
$VSWR_{in}$	input VSWR	adjust V_C for $P_L = 3$ W	–	–	2.5 : 1	
	stability	$P_D = 1.5$ to 6 mW; $V_S = 4$ to 6.5 V; $V_C = 0$ to 3.5 V; $P_L \leq 3$ W; VSWR ≤ 6 : 1 through all phases	–	–	–60	dBc
	isolation	$V_C = 0.5$ V	–	–	–36	dBm
	control bandwidth		1	–	–	MHz
P_n	noise power	$P_L = 3$ W; bandwidth = 30 kHz; 20 MHz above transmission band	–	–	–85	dBm
	ruggedness	$V_S = 6.5$ V; adjust V_C for $P_L = 3$ W; VSWR ≤ 10 : 1 through all phases	no degradation			



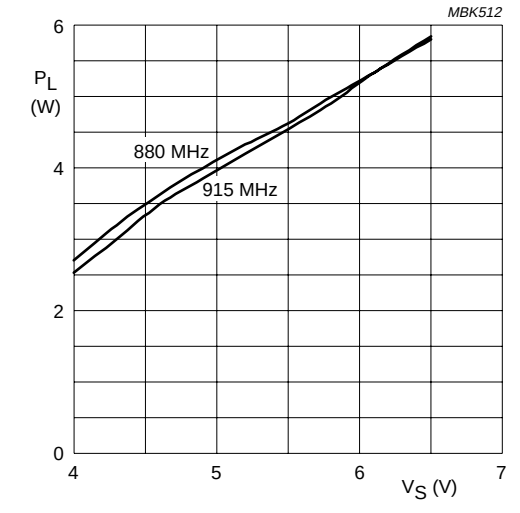
$Z_S = Z_L = 50\ \Omega$; $V_S = 4.8$ V; $P_D = 3$ mW;
$T_{mb} = 25$ °C; $\delta = 1 : 8$; $t_p = 575$ µs.

Fig.2 Load power as a function of control voltage; typical values.



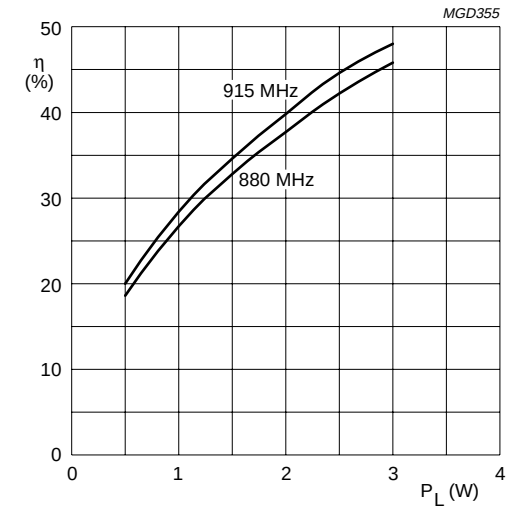
$Z_S = Z_L = 50\ \Omega$; $V_S = 4.8$ V; adjust V_C for $P_L = 3$ W;
$T_{mb} = 25$ °C; $\delta = 1 : 8$; $t_p = 575$ µs.

Fig.3 Load power as a function of drive power; typical values.



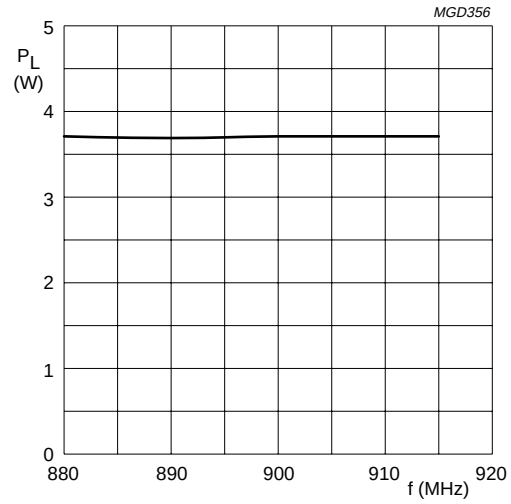
$Z_S = Z_L = 50\ \Omega$; $V_C = 3.5$ V; $P_D = 3$ mW;
$T_{mb} = 25$ °C; $\delta = 1 : 8$; $t_p = 575$ µs.

Fig.4 Load power as a function of supply voltage; typical values.



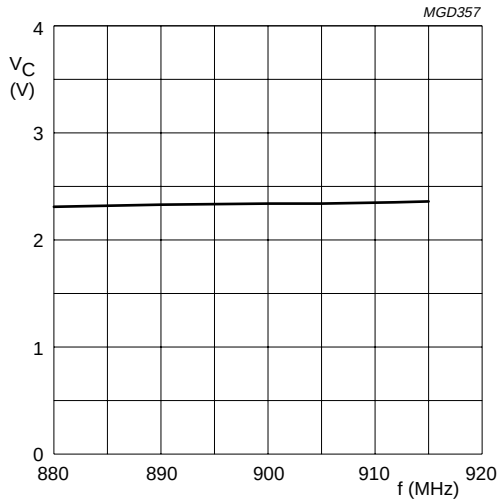
$Z_S = Z_L = 50\ \Omega$; $V_S = 4.8$ V; $P_D = 3$ mW;
$T_{mb} = 25$ °C; $\delta = 1 : 8$; $t_p = 575$ µs

Fig.5 Efficiency as a function of load power; typical values.



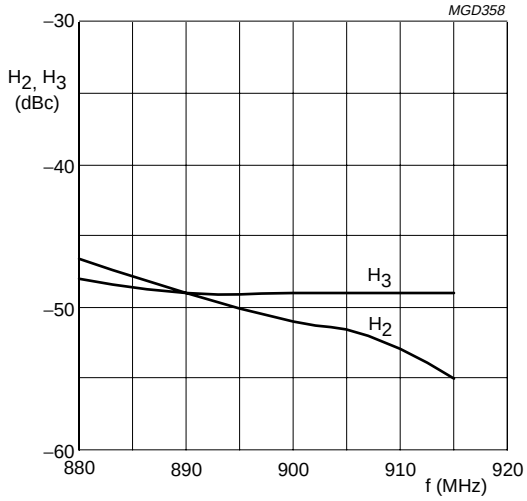
$Z_S = Z_L = 50\ \Omega$; $V_S = 4.8$ V; $P_D = 3$ mW; $V_C = 3.5$ V; $T_{mb} = 25$ °C; $\delta = 1 : 8$; $t_p = 575$ µs.

Fig.6 Load power as a function of frequency; typical values.



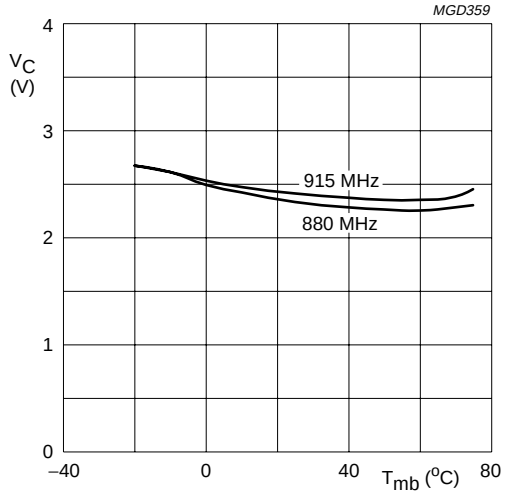
$Z_S = Z_L = 50\ \Omega$; $V_S = 4.8$ V; $P_D = 3$ mW; $P_L = 3$ W; $T_{mb} = 25$ °C; $\delta = 1 : 8$; $t_p = 575$ µs.

Fig.7 Control voltage as a function of frequency; typical values.



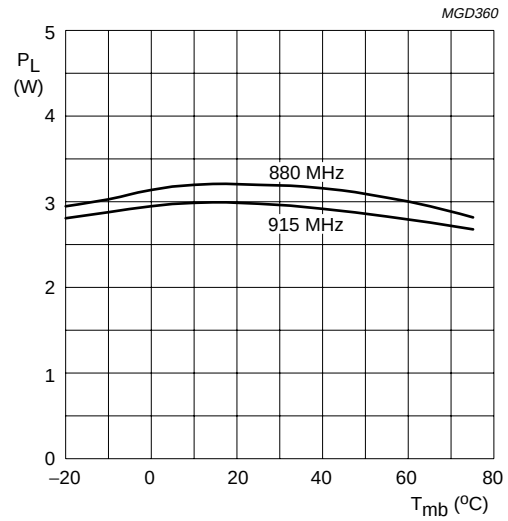
$Z_S = Z_L = 50\ \Omega$; $V_S = 4.8$ V; $P_D = 3$ mW; $P_L = 3$ W; $T_{mb} = 25$ °C; $\delta = 1 : 8$; $t_p = 575$ µs.

Fig.8 Harmonics as a function of frequency; typical values.



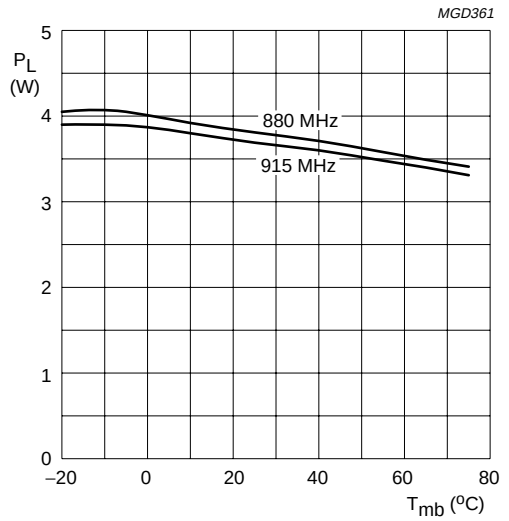
$Z_S = Z_L = 50\ \Omega$; $V_S = 4.8$ V; $P_D = 3$ mW; $P_L = 3$ W; $\delta = 1 : 8$; $t_p = 575$ µs.

Fig.9 Control voltage as a function of mounting base temperature; typical values.



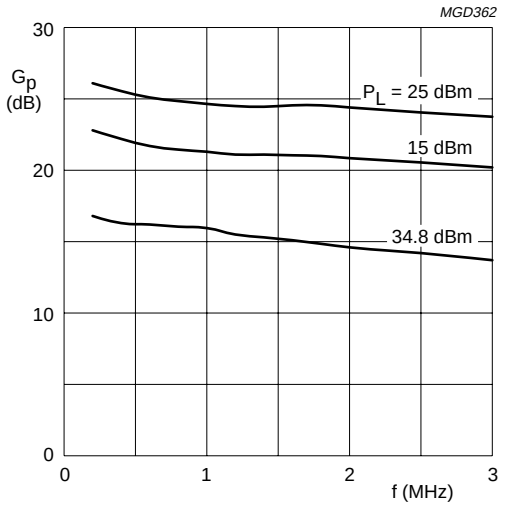
$Z_S = Z_L = 50\ \Omega$; $V_S = 4.3$ V; $P_D = 3$ mW; $V_C = 3.5$ V; $\delta = 1 : 8$; $t_p = 575\ \mu s$.

Fig.10 Load power as a function of mounting base temperature (reduced supply voltage); typical values.



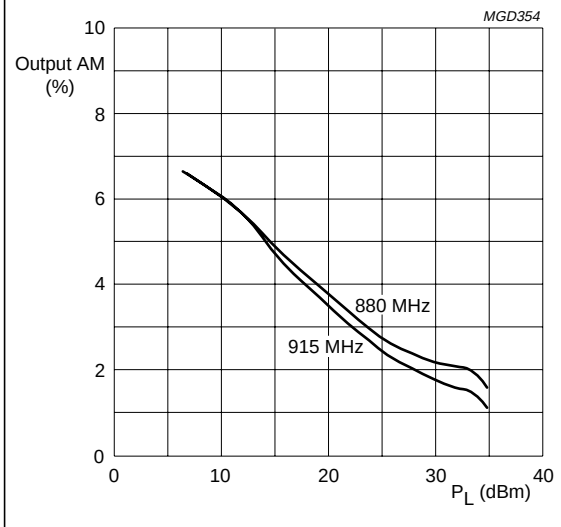
$Z_S = Z_L = 50\ \Omega$; $V_S = 4.8$ V; $P_D = 3$ mW; $V_C = 3.5$ V; $\delta = 1 : 8$; $t_p = 575\ \mu s$.

Fig.11 Load power as a function of mounting base temperature; typical values.



$Z_S = Z_L = 50\ \Omega$; $V_S = 4.8$ V; $P_D = 3$ mW; f = 900 MHz; C1 = 0; R1 = 100 Ω; $T_{mb} = 25$ °C; $\delta = 1 : 8$; $t_p = 575\ \mu s$.

Fig.12 Control loop power gain as a function of frequency on the control pin; typical values.



$Z_S = Z_L = 50\ \Omega$; $V_S = 4.8$ V; $P_D = 3$ mW; $T_{mb} = 25$ °C; input amplitude modulation = 3%; $\delta = 1 : 8$; $t_p = 575\ \mu s$.

Fig.13 Output amplitude modulation as a function of load power; typical values.

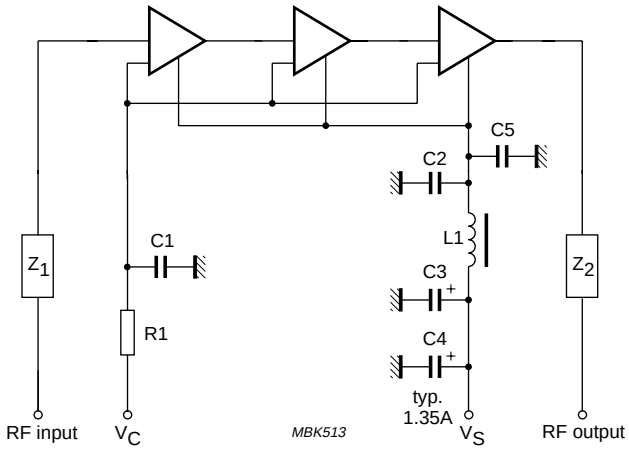
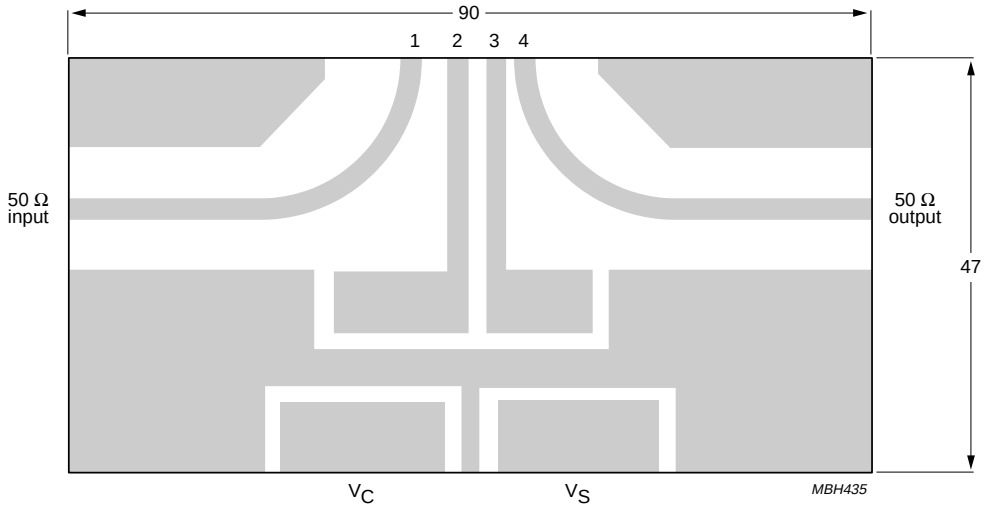


Fig.14 Test circuit.



Dimensions in mm.

Fig.15 Printed-circuit board layout.

List of components (See Fig 14)

COMPONENT	DESCRIPTION	VALUE	DIMENSIONS	CATALOGUE NO.
C1, C2	multilayer ceramic chip capacitor	680 pF		2222 851 11681
C3	tantalum capacitor	2.2 μF; 35 V		–
C4	electrolytic capacitor	47 μF; 40 V		2222 030 37479
C5	multilayer ceramic chip capacitor	100 nF		2222 852 47104
L1	Grade 4S2 Ferroxcube bead			4330 030 36300
Z_1, Z_2	stripline; note 1	50 Ω	width 2.33 mm	–
R1	metal film resistor	100 Ω; 0.6 W		2322 156 11001

Note

1. The striplines are on a double copper-clad printed-circuit board with PTFE fibreglass dielectric (ε_r = 2.2); thickness $^1/_{32}$ inch.

SOLDERING

The indicated temperatures are those at the solder interfaces.

Advised solder types are types with a liquidus less than or equal to 210 °C.

Solder dots or solder prints must be large enough to wet the contact areas.

Soldering can be carried out using a conveyor oven, a hot air oven, an infrared oven or a combination of these ovens. A double reflow process is permitted.

Hand soldering must be avoided because the soldering iron tip can exceed the maximum permitted temperature of 250 °C and damage the module.

The maximum allowed temperature is 250 °C for 5 seconds.

The maximum ramp-up is 10 °C per second.

The maximum cool-down is 5 °C per second.

Cleaning

The following fluids may be used for cleaning:

- Alcohol
- Bio-Act (Terpene Hydrocarbon)
- Acetone.

Ultrasonic cleaning should not be used since this can cause serious damage to the product.

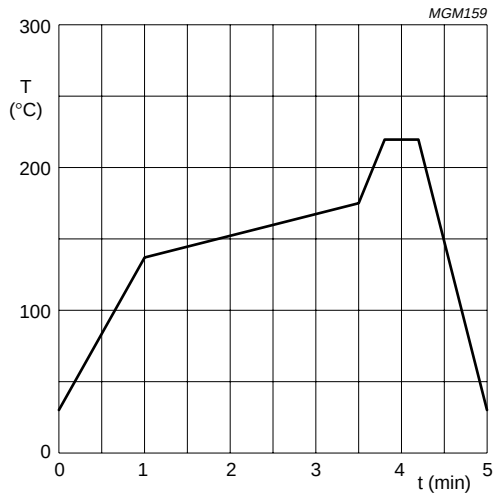
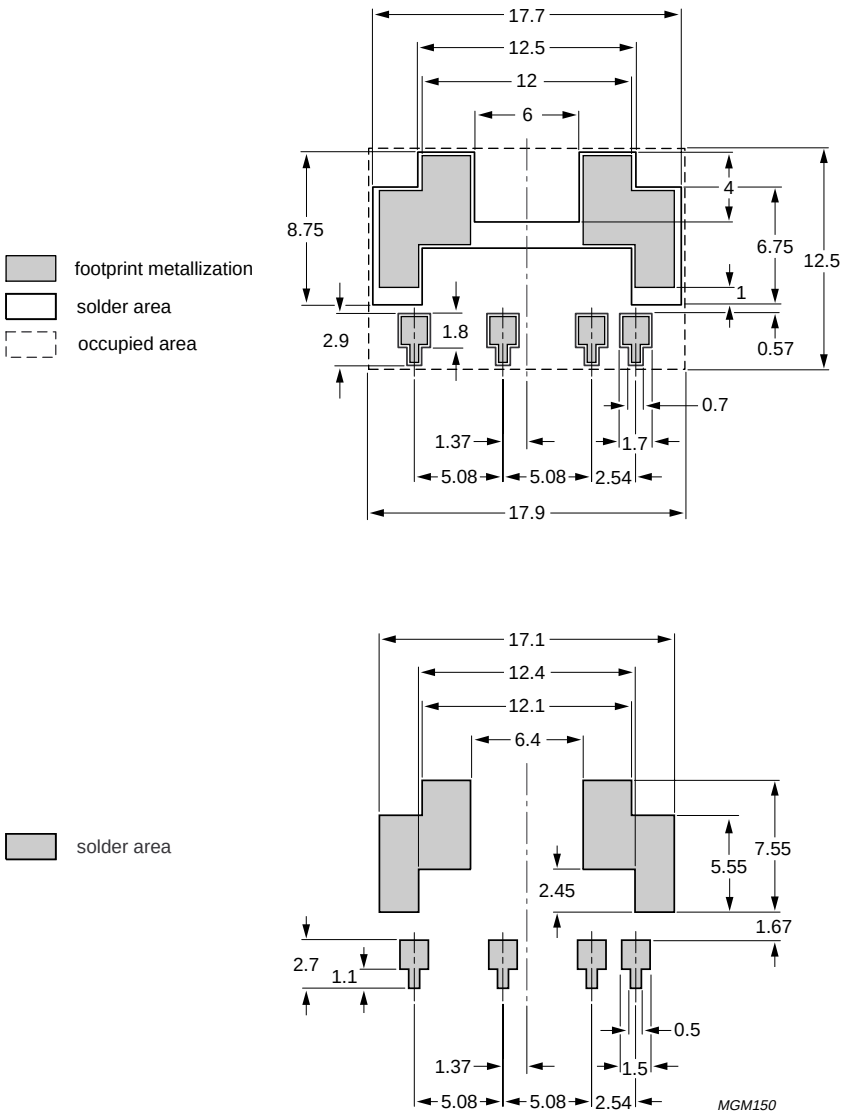


Fig.16 Recommended reflow temperature profile.

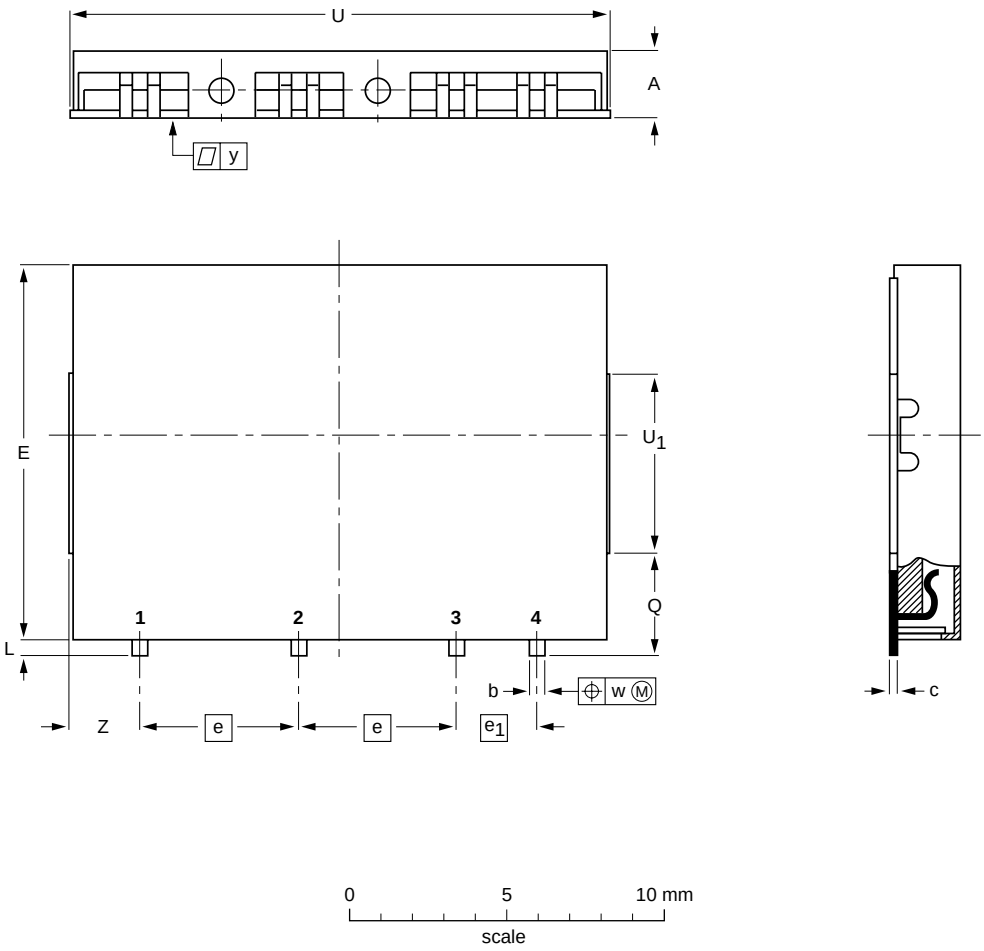


Dimensions in mm.

Fig.17 Footprint SOT388B.

PACKAGE OUTLINE

Rectangular single-ended surface-mount package; metal cap; 4 in-line leads **SOT388B**



DIMENSIONS (mm are the original dimensions)

UNIT	A	b	c	e	e_1	E	L	Q	U	U_1	w	y	Z
mm	2.2 1.8	0.56 0.46	0.30 0.20	5.08	2.54	12.2 11.8	0.7 0.3	3.4 3.0	17.3 16.9	6.0 5.6	0.25	0.15	2.3 1.9

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT388B						97-11-19

DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

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NOTES

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