

BMR - 0302

BMR-0302 is the low reset type of IC that guarantee to set again micro computers or logic systems by detecting the intermittent of fluctuating power supply voltage during normal use or switching on/off of the equipments.

A comparator type of hysteresis transistor developed by KODENSHI is built in the IC, so that BMR-0302 is very cost effective components. And, BMR-0302 is a super-low consumption electric current system reset IC which has been developed with using high resistance process.

FEATURES

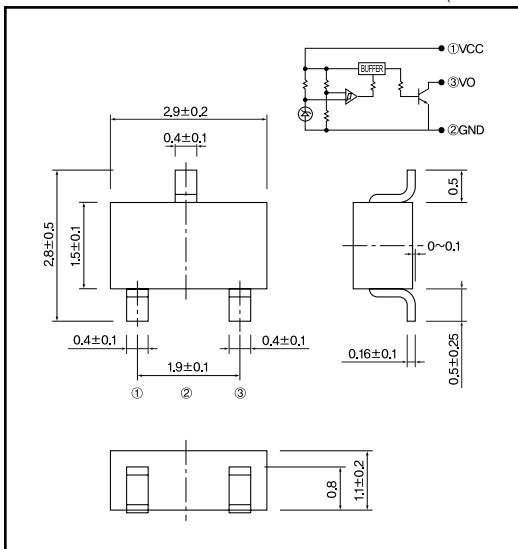
- Super-low current consumption
- Low operation voltage
- High current of output transistor
- Hysteresis circuit built in
- It has on delay function to supplement the constant of outer C and R
- A package is SOT-23 of the super-miniature surface mounting type

APPLICATIONS

- Micro computer circuits in mobile phones, word processors, TVs, VCRs etc.
- General logic circuits
- Detection of voltage drop in batteries of note personal computers, mobile phones
- Switching to backup power supply

DIMENSIONS

(Unit : mm)



MAXIMUM RATINGS

(Ta=25)

Item	Symbol	Rating	Unit
Supply voltage	V _{cc}	-0.3~+10.0	V
Power dissipation	P _b	150	mW
Operating temp.	Topr.	-20~+75	
Storage temp.	Tstg	-40~+125	

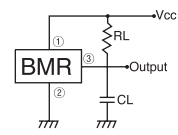
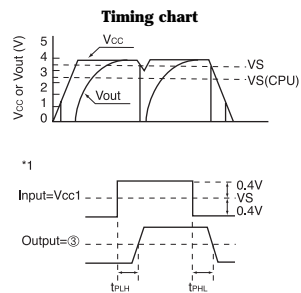
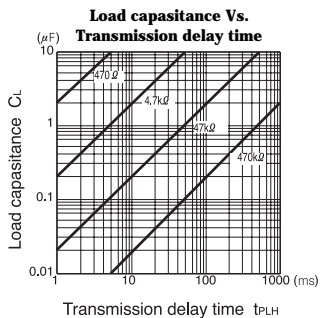
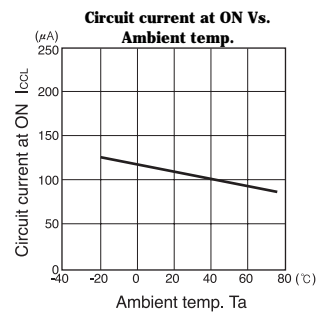
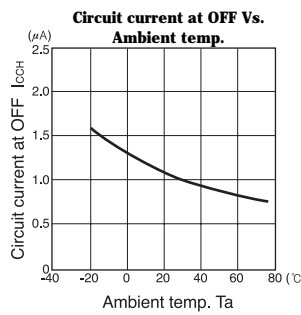
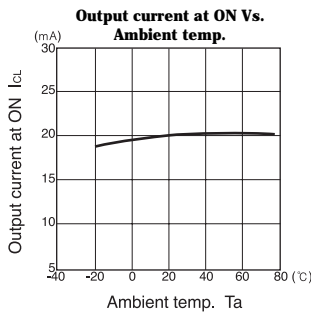
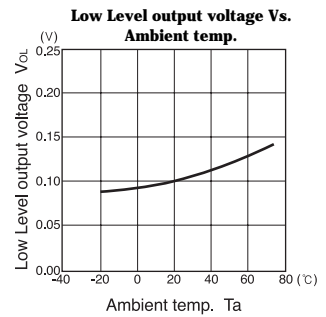
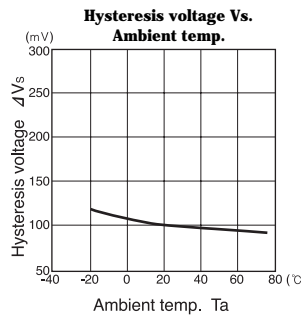
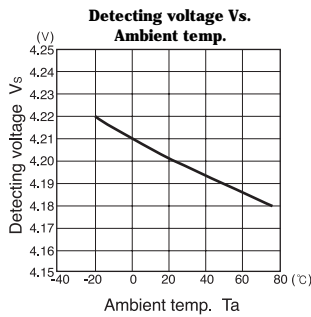
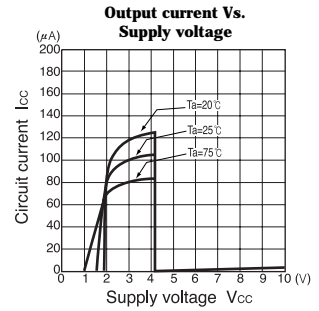
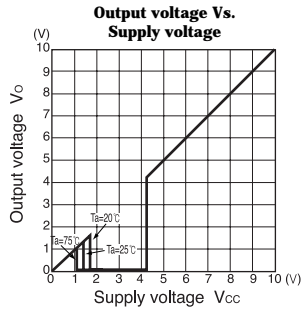
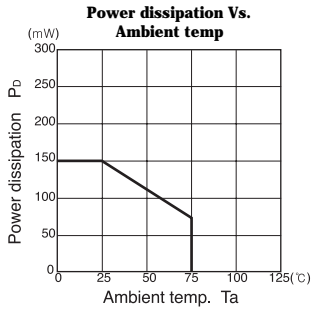
ELECTRO-OPTICAL CHARACTERISTICS

(V_{cc}=5V, Ta=25)

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit.
Detecting Voltage	BMR - 0302C	R _L = 470 Ω, V _{cc} = H, V _{OL} = 0.4V	4.3	4.5	4.7	V
	BMR - 0302D		4.0	4.2	4.4	
	BMR - 0302E		3.7	3.9	4.1	
	BMR - 0302F		3.4	3.6	3.8	
	BMR - 0302G		3.1	3.3	3.5	
	BMR - 0302H		2.9	3.1	3.3	
	BMR - 0302I		2.75	2.9	3.05	
	BMR - 0302J		2.55	2.7	2.85	
	BMR - 0302K		2.35	2.5	2.65	
	BMR - 0302L		2.15	2.3	2.45	
Hysteresis voltage	V _s	R _L = 470 Ω, V _{cc} = L, H, L	40	100	300	mV
Temperature coefficient of detecting voltage	V _s / T	R _L = 470 Ω, Ta = -20 ~ 75	-	±0.01	-	%/°C
Low level output voltage	V _{OL}	R _L = 470 Ω, V _{cc} = V _s Min.	-	0.1	0.4	V
Circuit current at ON	I _{CC1}	R _L = ∞, V _{cc} = V _s Min.	-	100	180	μA
Circuit current at OFF	I _{CC2}	R _L = ∞, V _{cc} = V _s Max+0.1V	-	1.0	2.5	μA
Threshold operating voltage	V _{OPL}	R _L = 4.7k Ω, V _{OL} = 0.4V	-	1.4	1.6	V
Output current at ON 1	I _{OL1}	R _L = 0 Ω, V _{cc} = V _s Min.	10	20	-	mA
Output current at ON 2	I _{OL2}	R _L = 0 Ω, Ta = -20 ~ 75	5	-	-	mA
Transmission delay time	t _{PLH}	R _L = 4.7k Ω, C _L = 100pF	-	100	500	μsec
Transmission delay time	t _{PHL}	R _L = 4.7k Ω, C _L = 100pF	-	10	20	μsec

System Reset IC

BMR - 0302



V_{CC} : V
 $V_{S(CPU)}$: Reset threshold voltage of CPU, MPU
 CL : μF
 RL : $K\Omega$
 Caution) It is desirable that Capacitor be built between ① and ② terminal when high impedance of V_{CC} line, unstable power line or high ripple occurrence to be expected.